

COMPAL CONFIDENTIAL

MODEL NAME : VAL90

PCB NO : DA6000WZ010

GPIO P/N: 2012.8.1 Rev 2.7

BOM P/N :

X76 of SPI:

X7650731L03

SA000039A30 (64M W25Q64FVSSIQ)

SA00003K820 (32M W25Q32FVSSIQ)

X7650731L04

SA00006N000 (32M MX25L3273EM2I-10G)

SA00006N100 (64M MX25L6473EM2I-10G)

SALADO 14 HSW UMA

HASWELL + LYNX POINT

2013-07-04

REV : 1.0 (A00)

@ : Nopop Component

CONN@ : Connector Component

SPI@ : For X76 SPI

14UMA

EMC@: EMI/RF/ESD ask to add

V

CXDP@ : CPU XDP

PXDP@ : PCH XDP

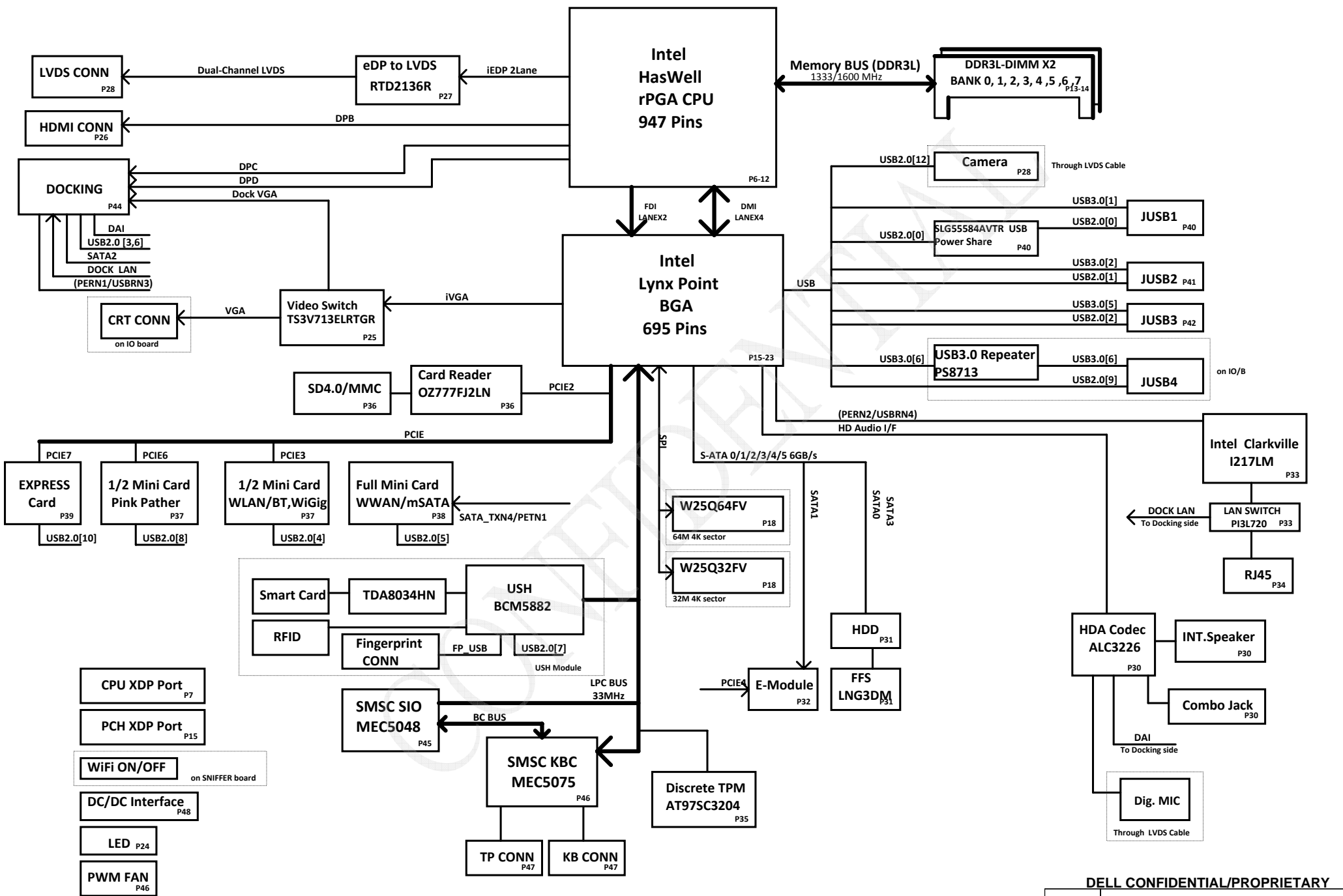
Part Number	Description
DA6000WZ000	PCB OYG LA-9931P REV0 M/B UMA 6

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Block Diagram



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POWER STATES

Signal State	SLP S3#	SLP S4#	SLP S5#	SLP A#	ALWAYS PLANE	M PLANE	SUS PLANE	RUN PLANE	CLOCKS
S0 (Full ON) / M0	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S3 (Suspend to RAM) / M3	LOW	HIGH	HIGH	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to DISK) / M3	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF	OFF
S5 (SOFT OFF) / M3	LOW	LOW	LOW	HIGH	ON	ON	OFF	OFF	OFF
S3 (Suspend to RAM) / M-OFF	LOW	HIGH	HIGH	LOW	ON	OFF	ON	OFF	OFF
S4 (Suspend to DISK) / M-OFF	LOW	LOW	HIGH	LOW	ON	OFF	OFF	OFF	OFF
S5 (SOFT OFF) / M-OFF	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF	OFF

PM TABLE

State	power plane	+PWR_SRC +PWR_SRC_S +5V_ALW +3.3V_ALW +3.3V_ALW_PCH +3.3V_RTC_LDO	+3.3V_SUS +1.35V_MEM	+5V_RUN +3.3V_RUN +1.5V_RUN +0.675V_DDR_VTT +VCC_CORE +1.05V_RUN	+3.3V_M +1.05V_M	+3.3V_M +1.05V_M (M-OFF)
S0	ON	ON	ON	ON	ON	ON
S3	ON	ON	ON	OFF	ON	OFF
S5 S4/AC	ON	OFF	OFF	OFF	ON	OFF
S5 S4/AC don't exist	OFF	OFF	OFF	OFF	OFF	OFF

USB 2.0 PORT#	USB 3.0 PORT#	DESTINATION
0	1	Rear Side (JUSB1)
1	2	Right Side TOP (JUSB2)
2	5	Right Side bottom (JUSB3)
3	3 (PERN1/USBRN3)	DOCKING (JDOCK1)
4		WLAN (JMINI2)
5		WWAN (JMINI1)
6		DOCKING (JDOCK1)
7		USH (JUSH1)
8		Pink Pather (JMINI3)
9	6	Left Side (JUSB4)
10		Express card (JEXP1)
11		None
12		CAMERA (JCAM1)
13		LCD Touch

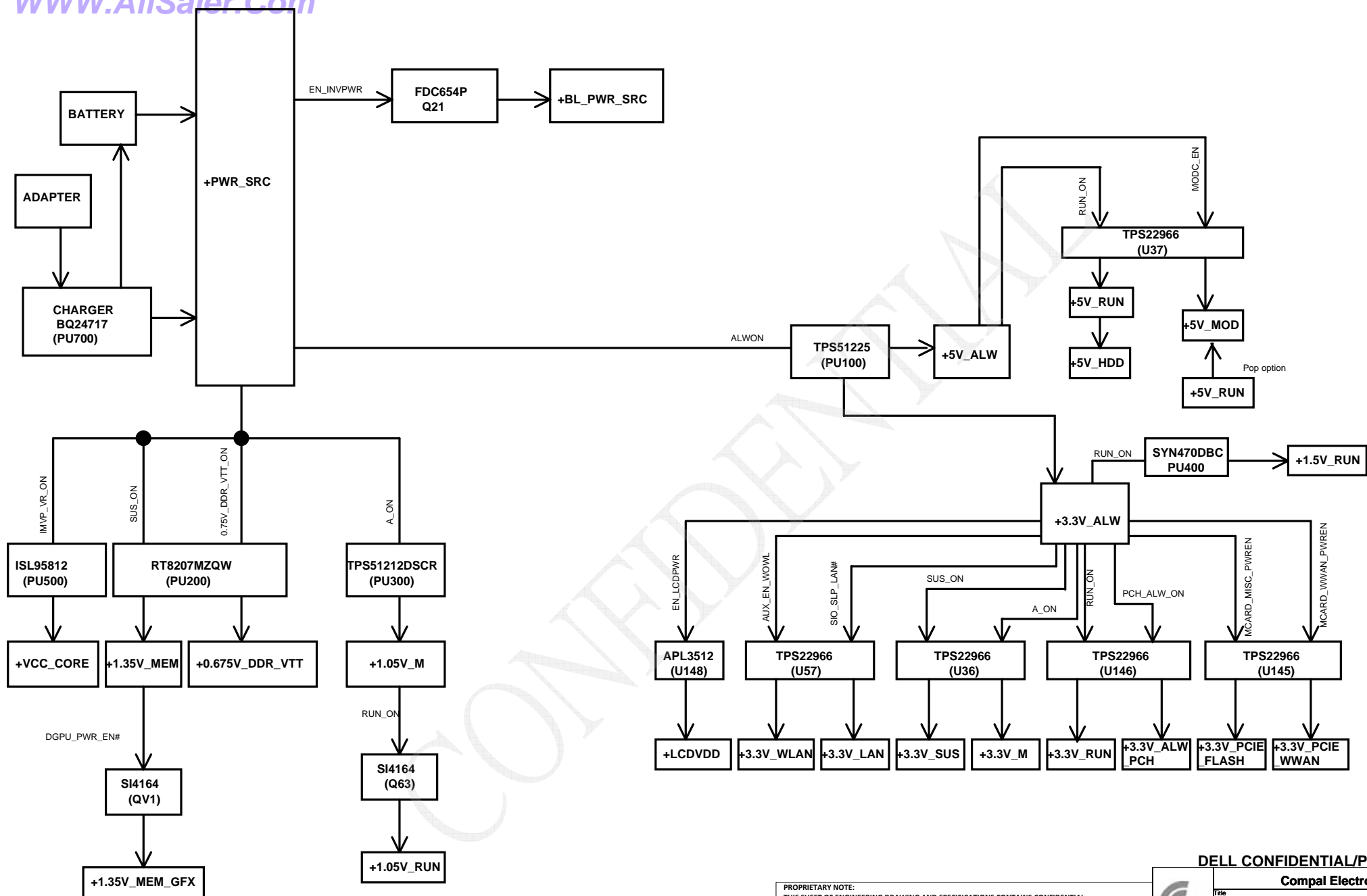
PCI EXPRESS	DESTINATION
Lane 1 (SATA_TXN4/PETN1)	WWAN (JMINI1) SATA by default
Lane 2 (SATA_RXN5/PERN2)	None
Lane 2 (PERN2/USBRN4)	10/100/1G LOM
Lane 3	WLAN (JMINI2)
Lane 4	E3 Module Bay (JSATA2)
Lane 5	None
Lane 6	Pink Pather (JMINI3)
Lane 7	Express card (JEXP1)
Lane 8	MMI

SATA	DESTINATION
SATA 0	HDD (JSATA1)
SATA 1	ODD (JSATA2)
SATA 2	Dock (JDOCK1)
SATA 3	NA
SATA 4 (SATA_TXN4/PETN1)	WWAN (JMINI1) SATA by default

DISPLAY Ports On CPU	Connetion
DDIB	MB HDMI (JHDMI1)
DDIC	Dock DP port 1
DDID	Dock DP port 2

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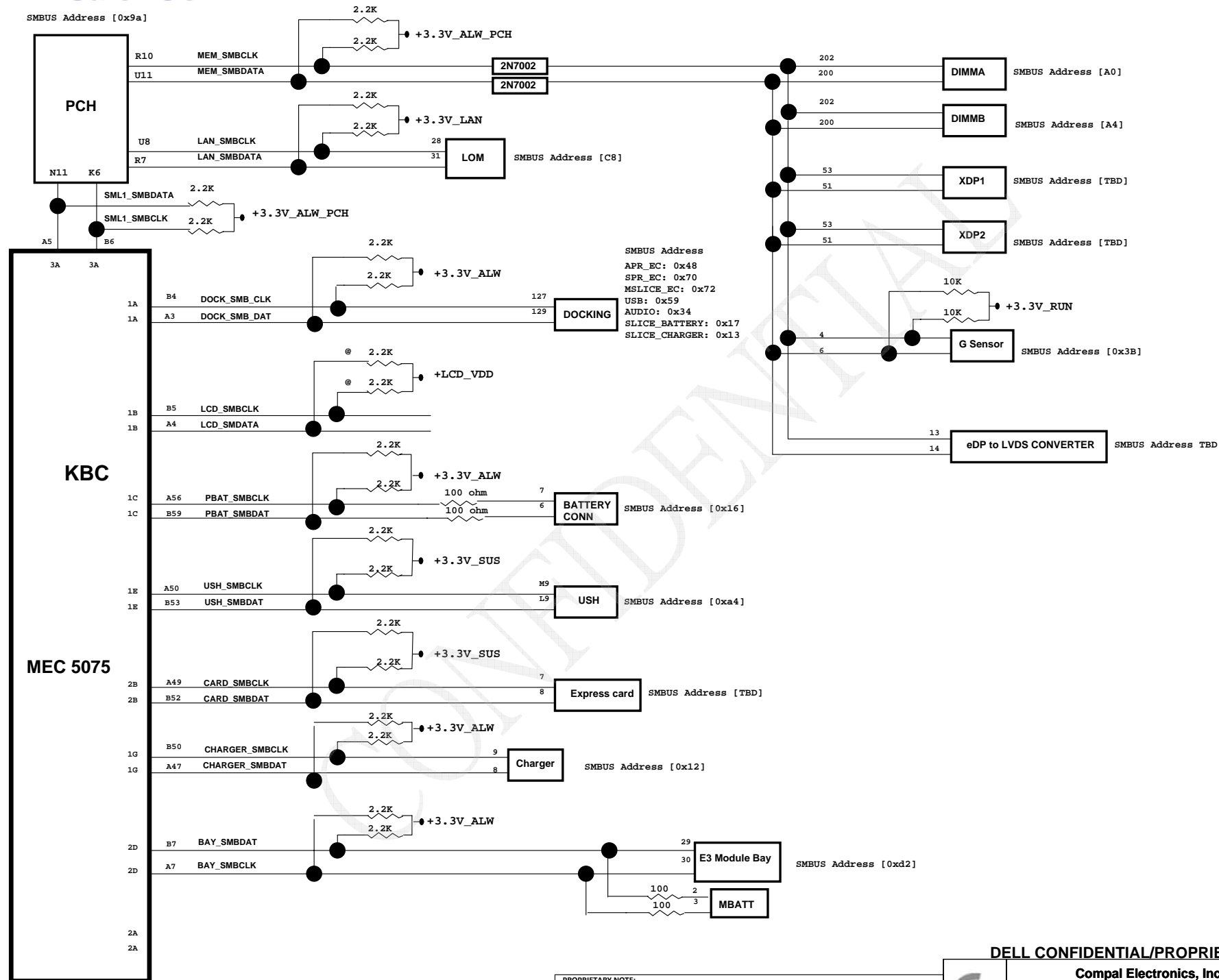
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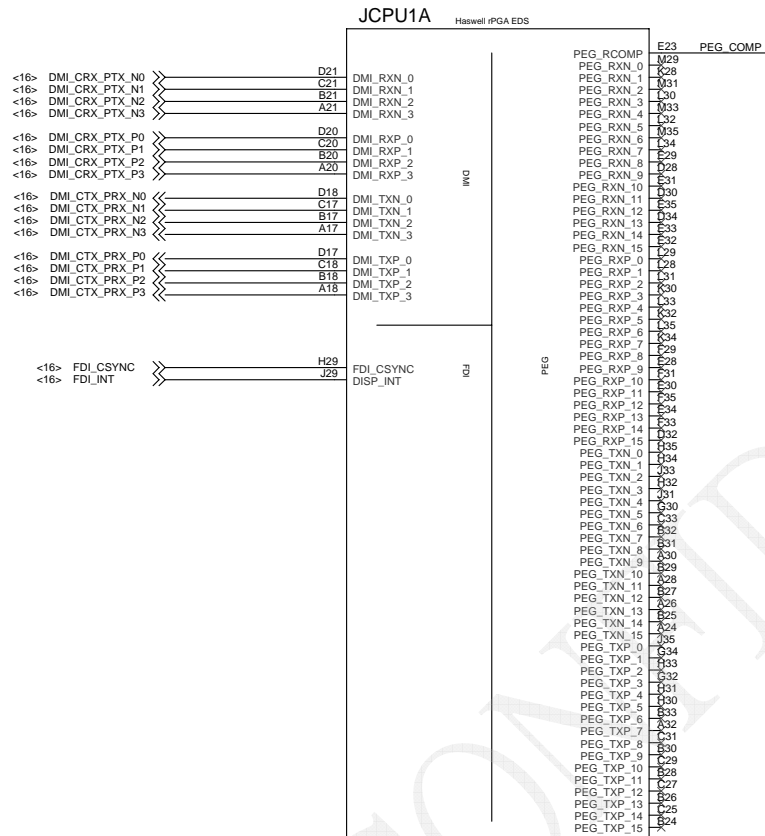
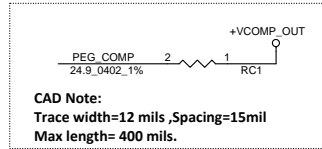
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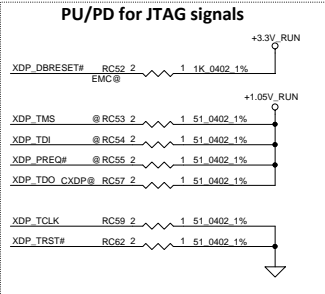
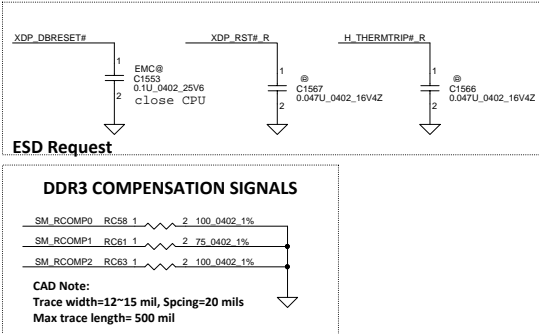
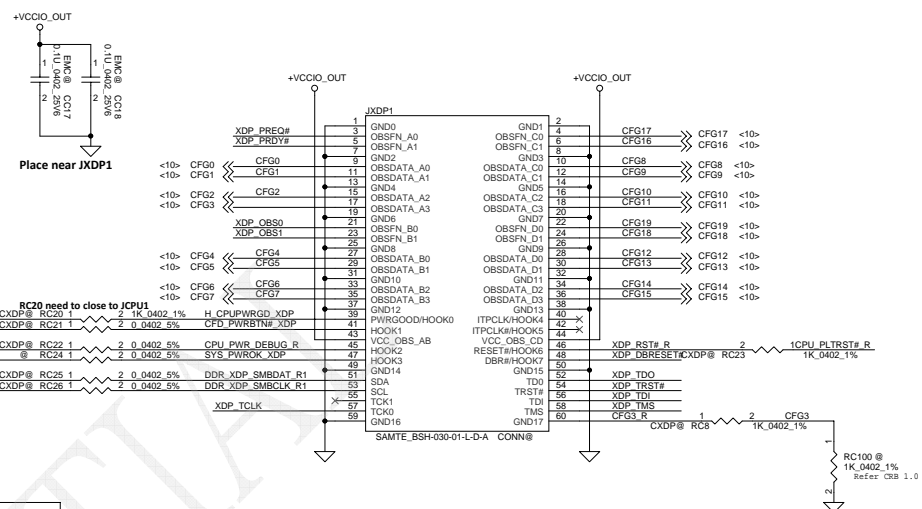
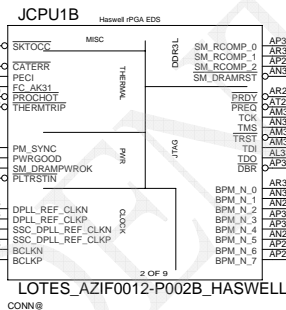
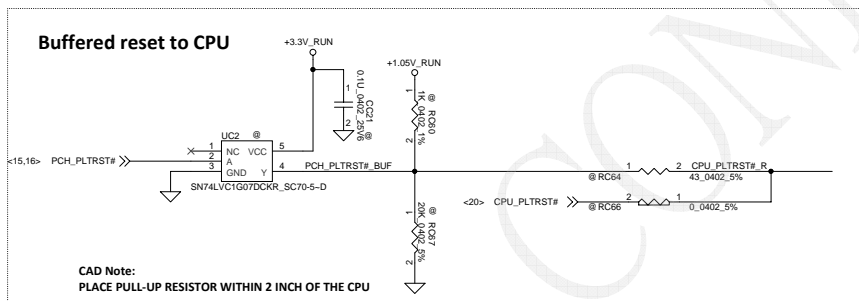
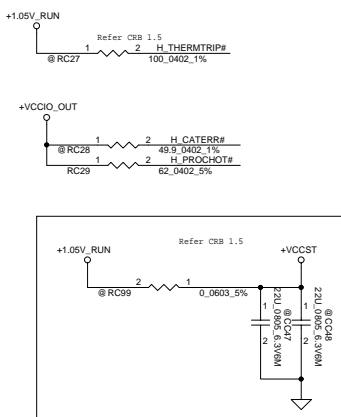
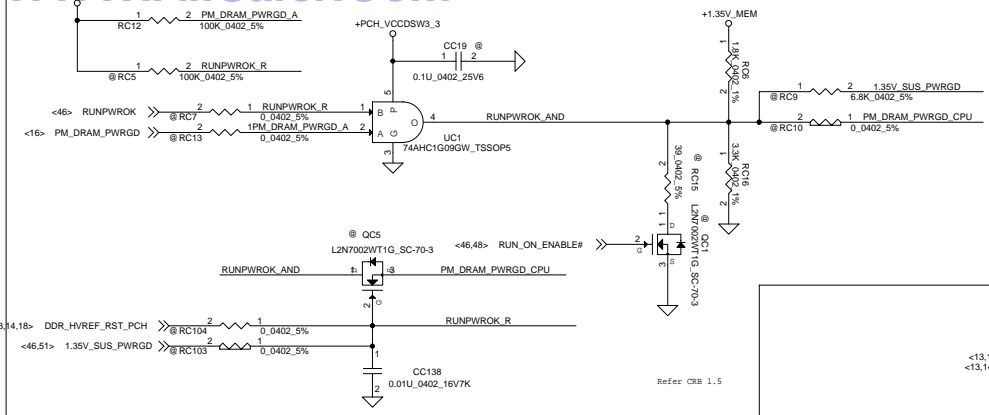
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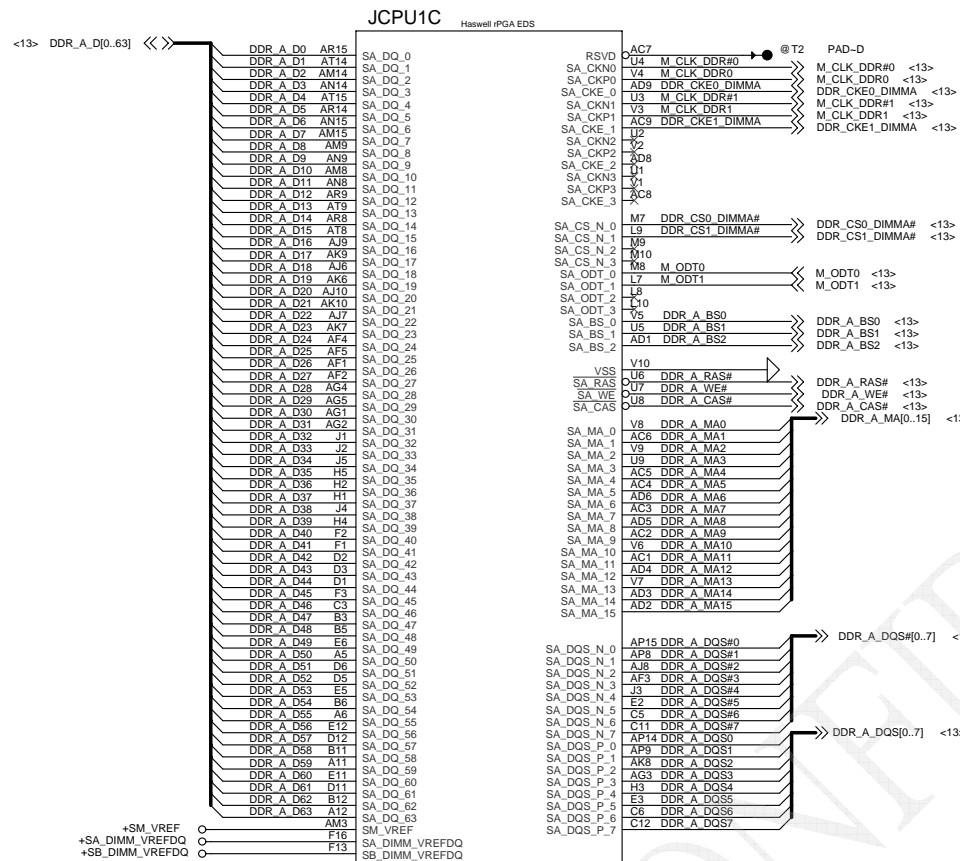
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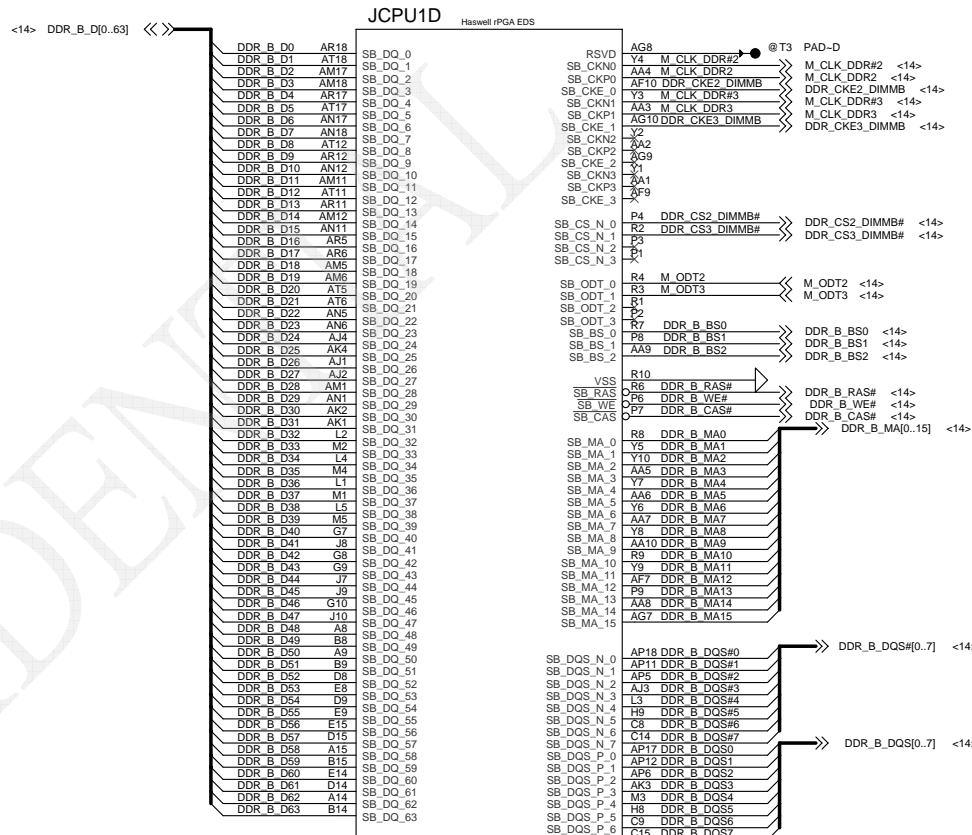
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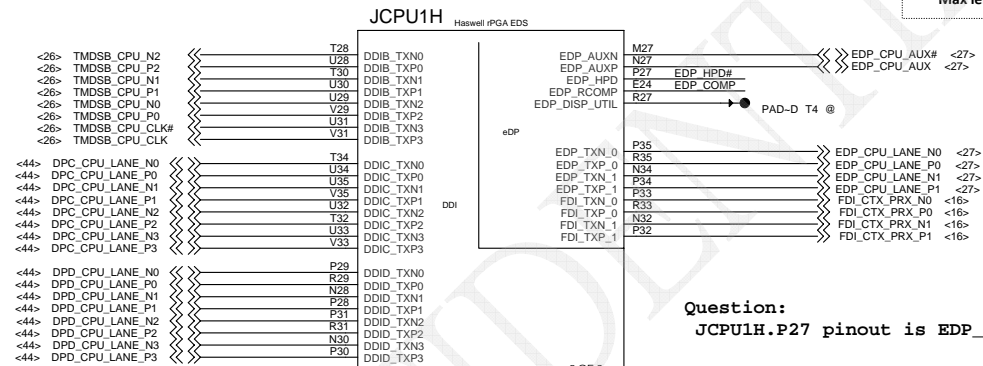
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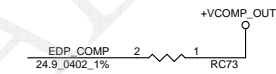
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COMPENSATION PU FOR eDP

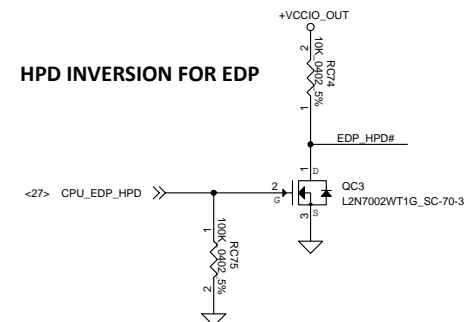


CAD Note: Trace width=20 mils, Spacing=25mil, Max length=100 mils.

Question:

JCPU1H.P27 pinout is EDP_HPDI. is it high active when Plug in eDP device?

HPDI INVERSION FOR EDP



QC3 change PN to SB50138003L S TR BSS138-7-F 1N SOT23-3

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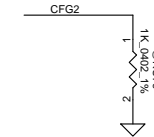
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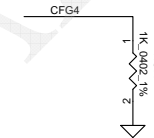
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CFG STRAPS for CPU



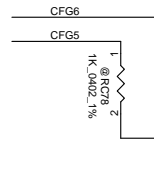
PEG Static Lane Reversal - CFG2 is for the 16x

CFG2	1:(Default) Normal Operation; Lane # definition matches socket pin map definition 0:Lane Reversed
------	--



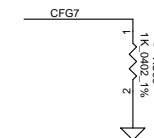
Display Port Presence Strap

CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port
------	--



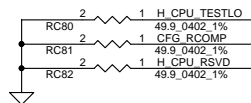
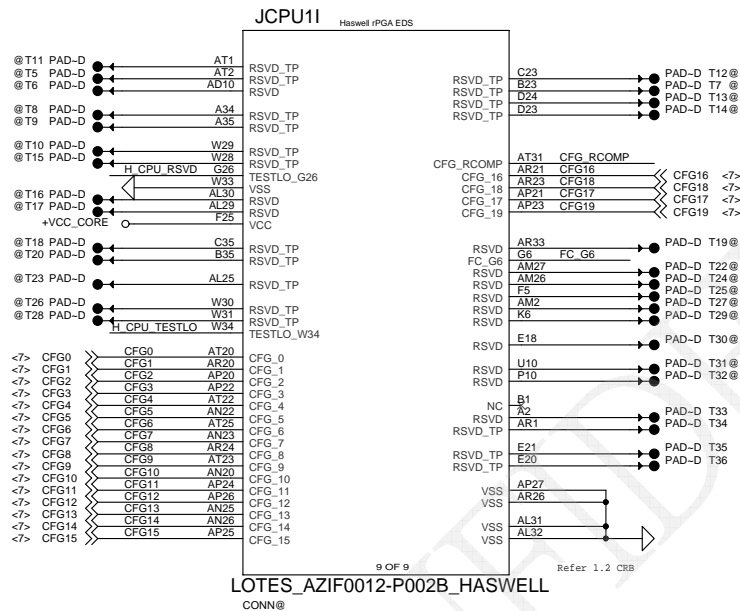
PCIe Port Bifurcation Straps

CFG[6:5]	11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled
----------	--

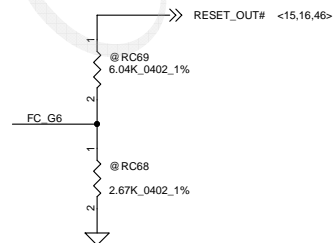


PEG DEFER TRAINING

CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training
------	---



Note: Reserve this circuit for future compatibility

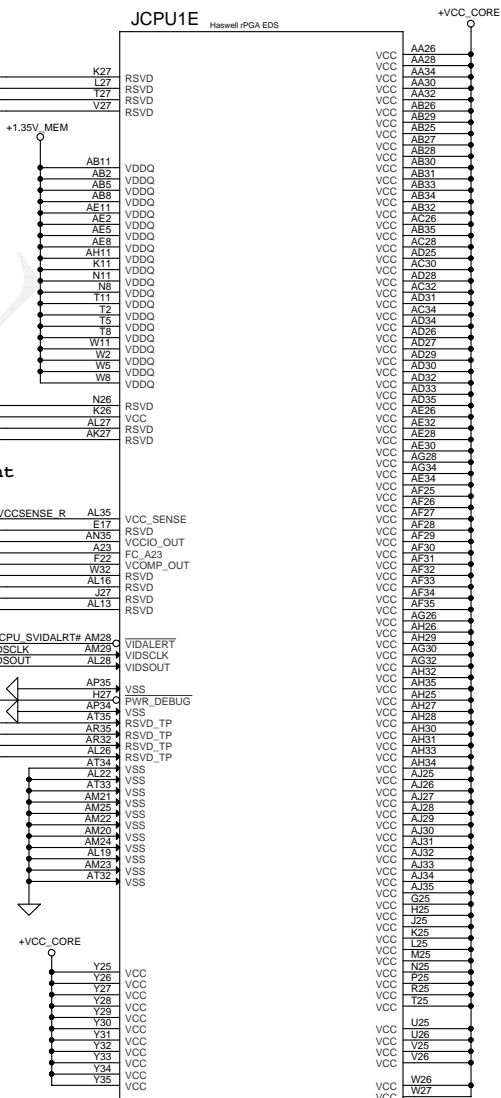
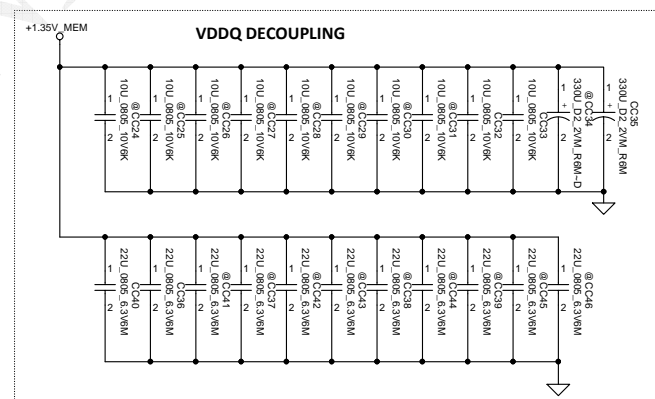
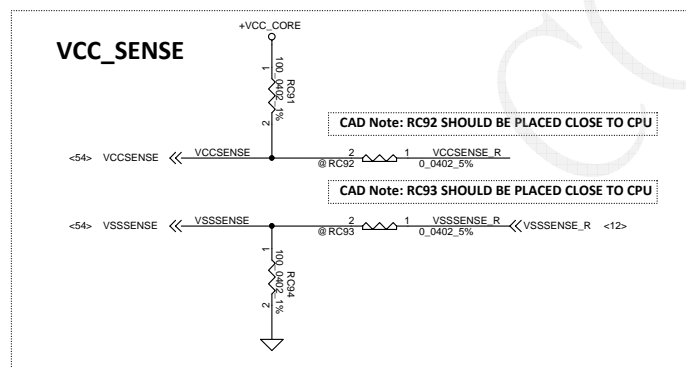
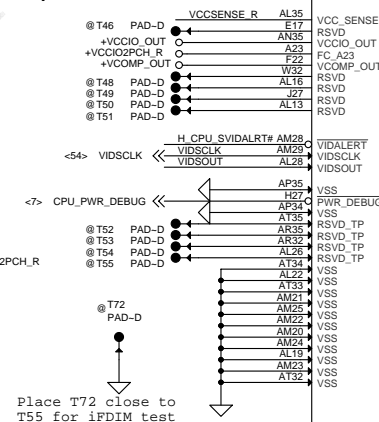
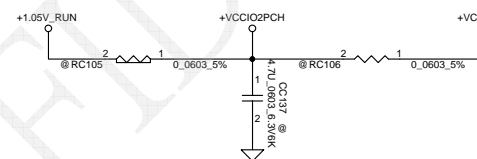
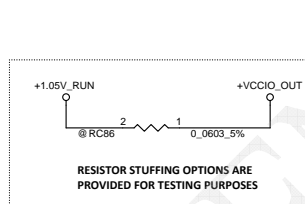


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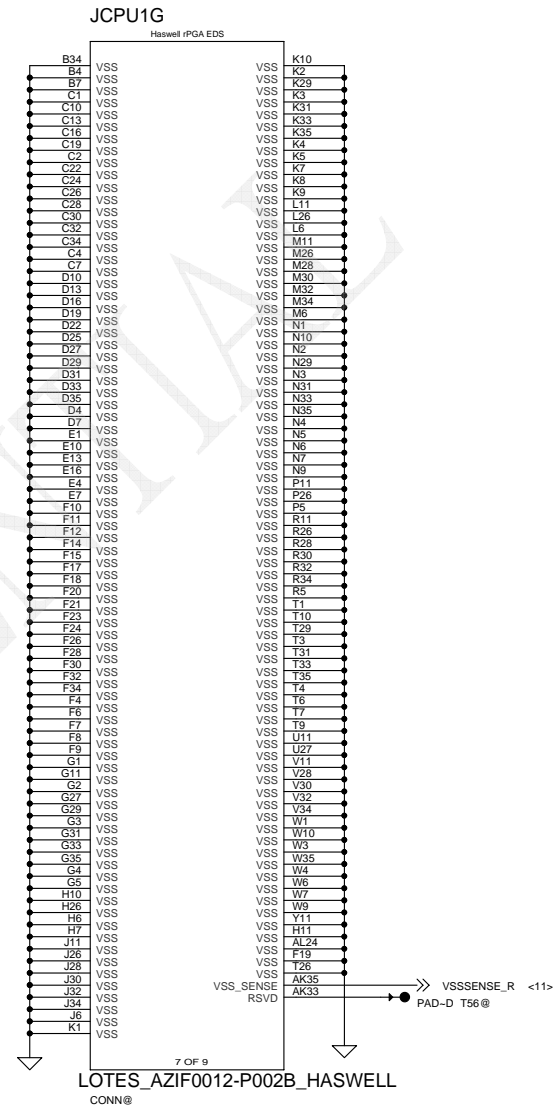
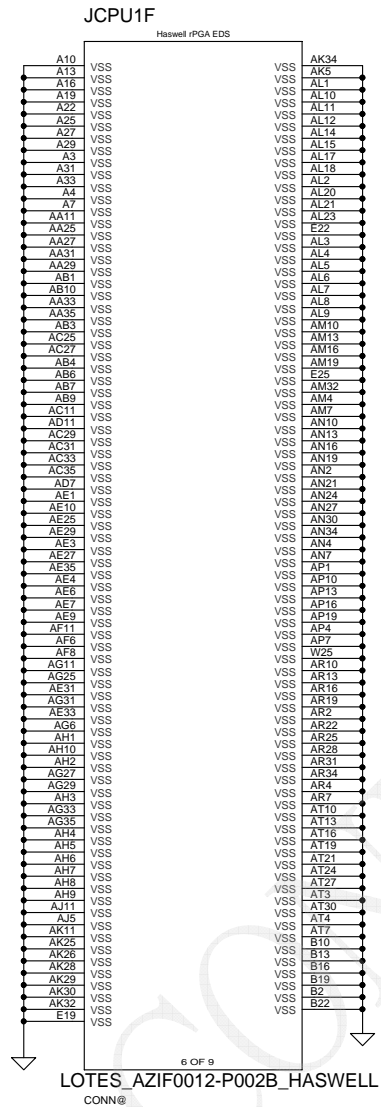
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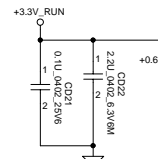
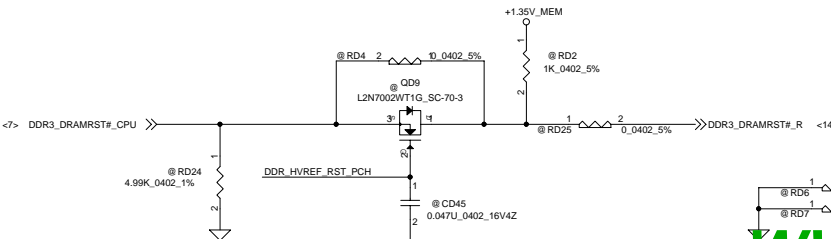
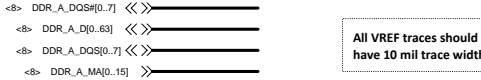
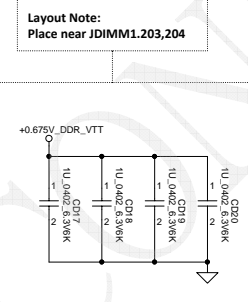
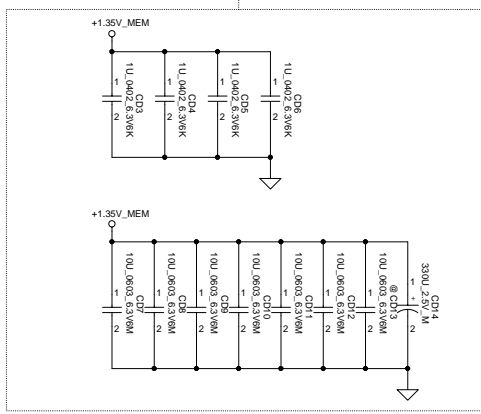
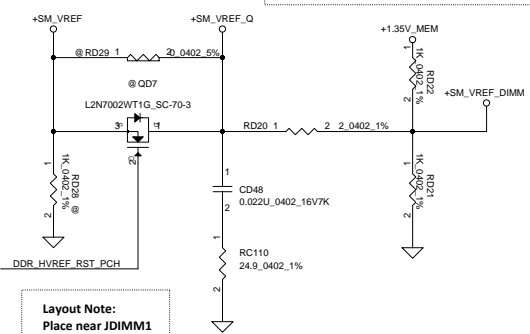
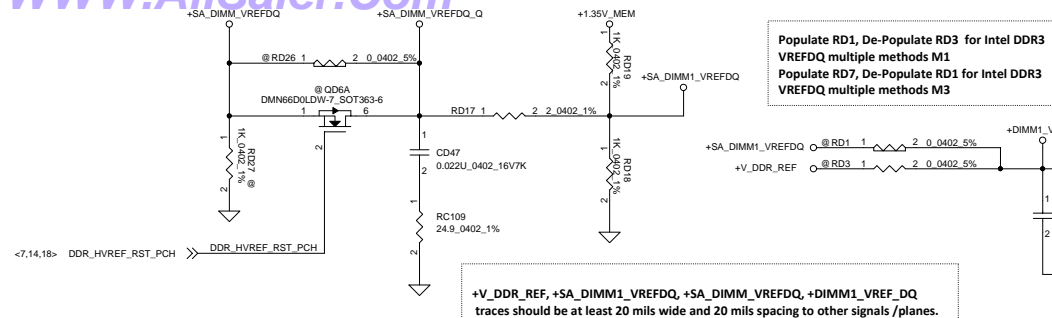


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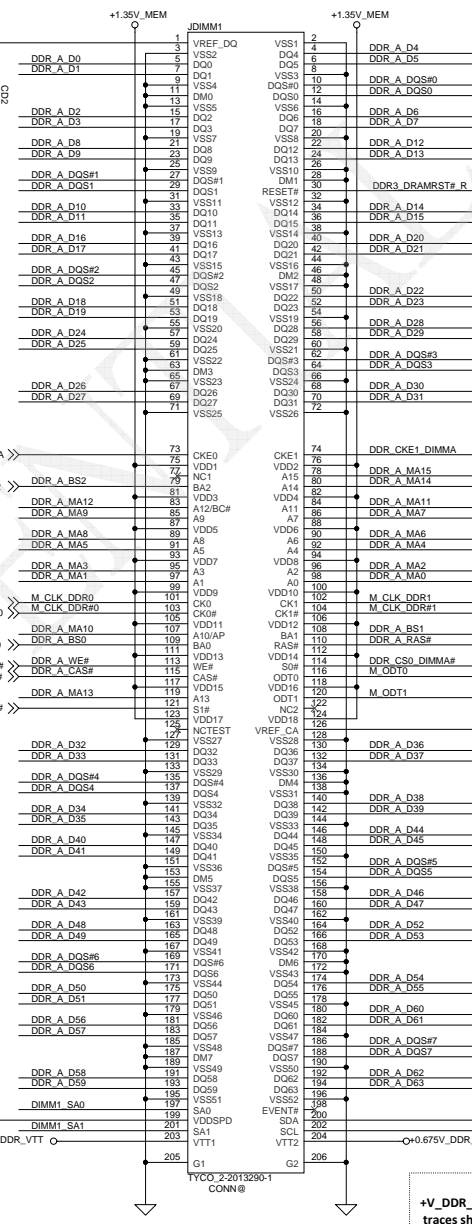
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JDIMM1 H=5.2mm



+V_DDR_REF, +SM_VREF_DIMM, +DIMM1_VREF_CA, +SM_VREF, +DIMM1_VREF_CA traces should be at least 20 mils wide and 20 mils spacing to other signals /planes.

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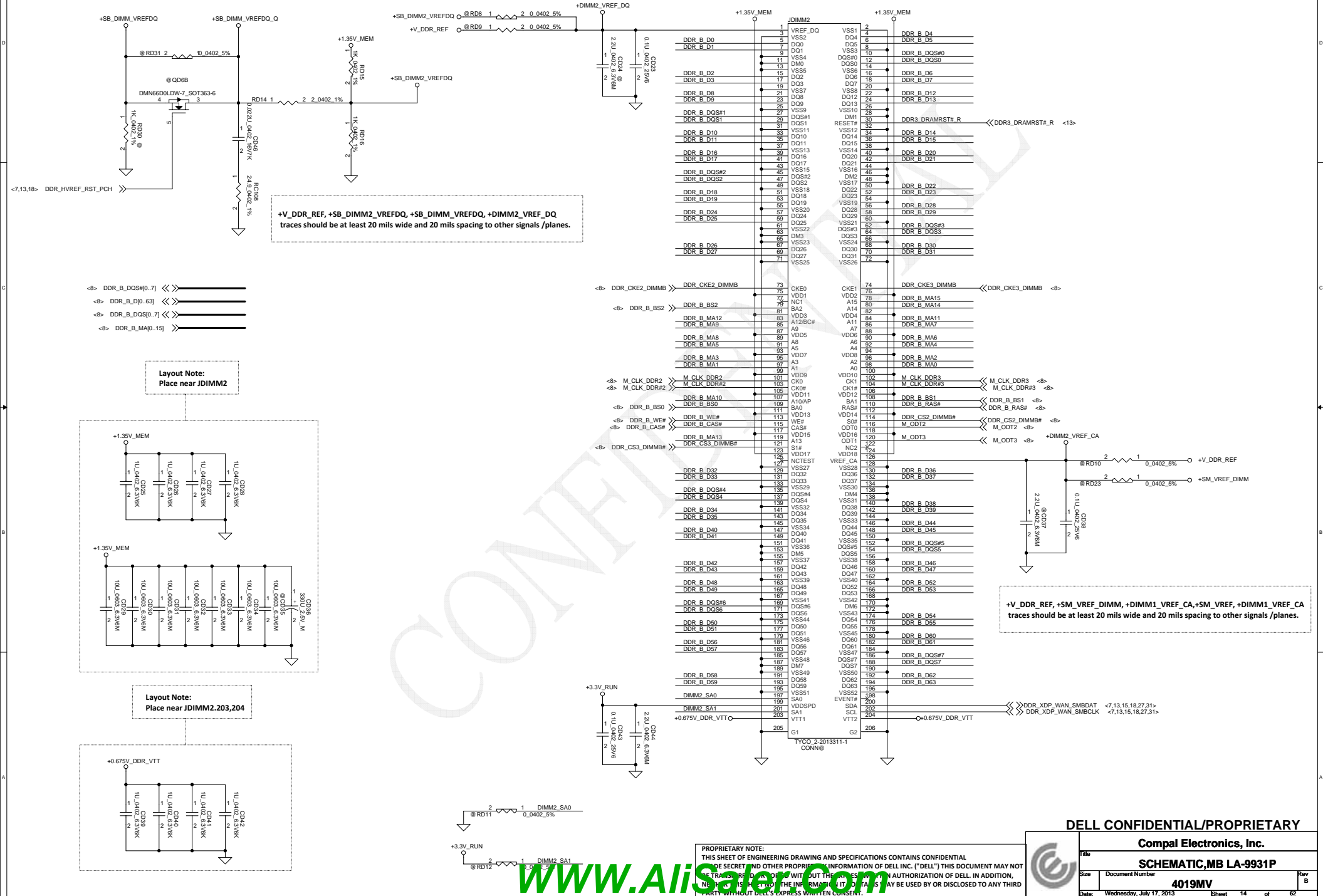
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Populate RD4, De-Populate RD9 for Intel DDR3
VREFDQ multiple methods M1
Populate RD8, De-Populate RD8 for Intel DDR3
VREFDQ multiple methods M3

JDIMM2 H=9.2mm

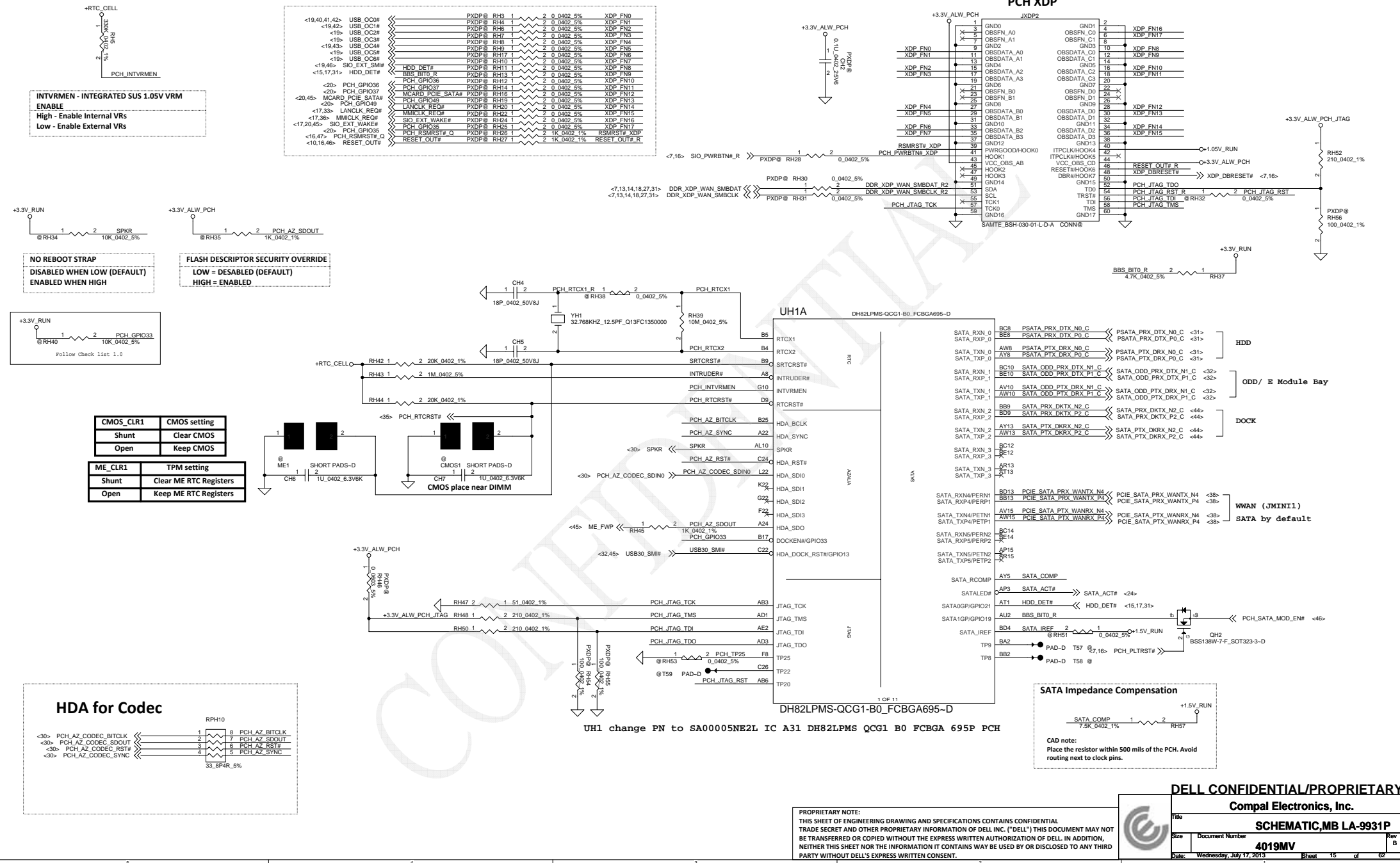


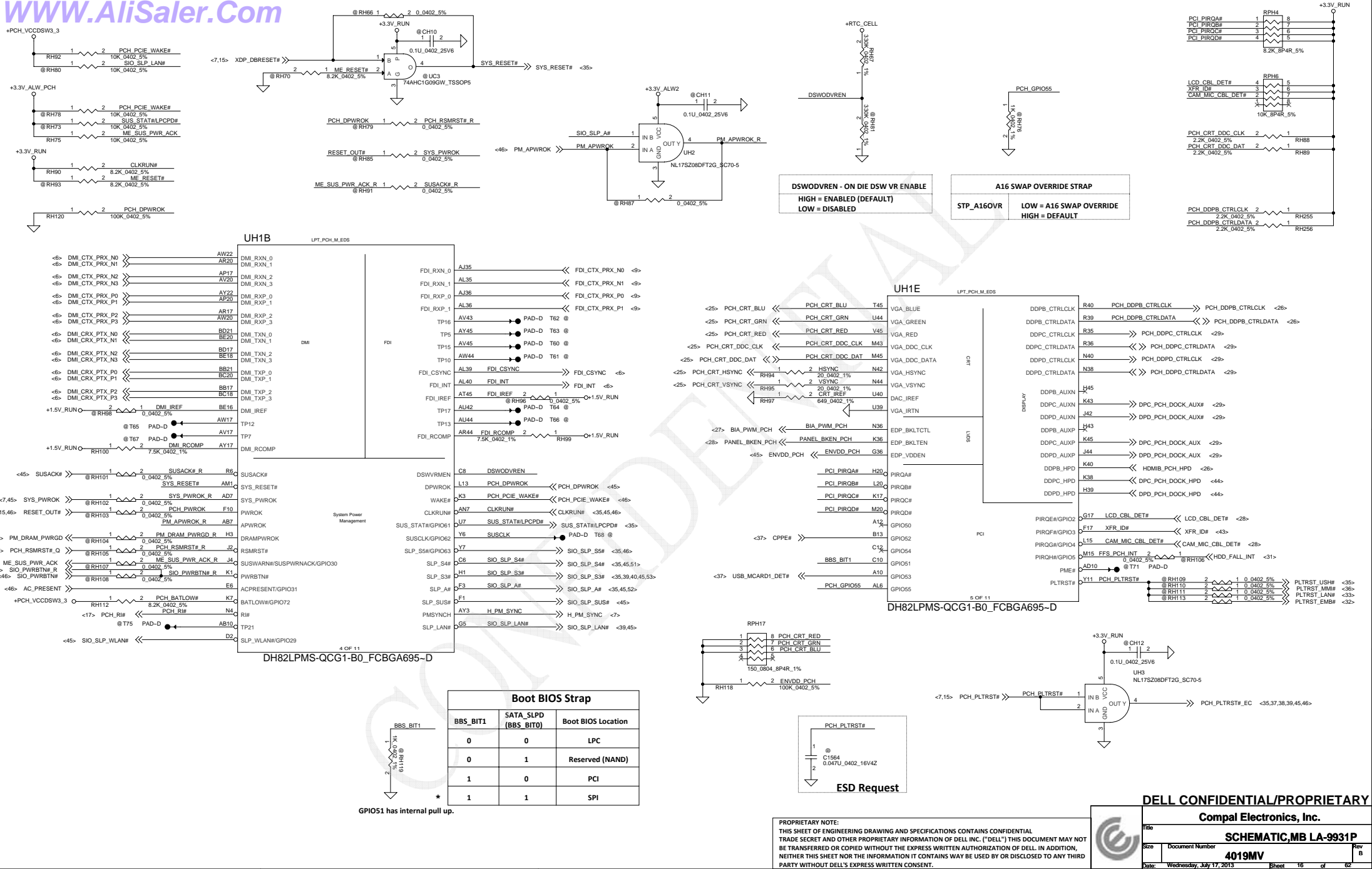
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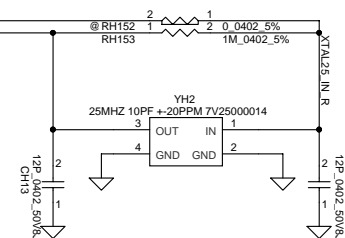
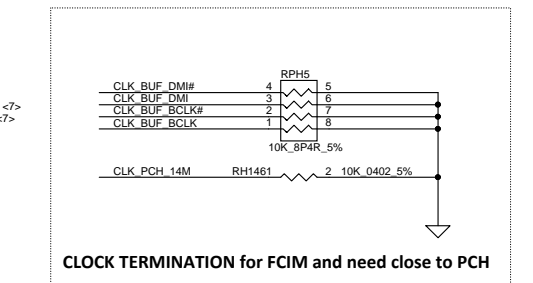
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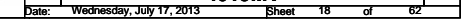
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U11

LPT_PCH_MLDS

MLK DOCK ---->

10/100/1G LAN ---->

WLAN (JMINI2)---->

E3 Module Bay---->

Pink Pather (JMINI3)---->

EXPRESS Card---->

MMI ---->

PCIE

USB

PCIE_IREF

PCIE_RCOMP

9 OF 11

DH82LPMS-QCG1-B0_FCBGA695-D

USB2N0 B37

USB2P0 D37

USB2N1 A38

USB2P1 C38

USB2N2 A36

USB2P2 C36

USB2N3 A34

USB2P3 C34

USB2N4 B33

USB2P4 D33

USB2N5 F31

USB2P5 G31

USB2N6 K31

USB2P6 L31

USB2N7 H29

USB2P7 A32

USB2N8 B32

USB2P8 C32

USB2N9 C30

USB2P9 B29

USB2N10 D29

USB2P10 C28

USB2N11 G26

USB2N12 F26

USB2P12 F24

USB2N13 G24

USB3RN1 AR26

USB3RP1 AP26

USB3TN1 BE24

USB3TP1 BD23

USB3RN2 AW26

USB3RP2 AV26

USB3TN2 BD25

USB3TP2 AW29

USB3RN5 BC26

USB3RP5 BE28

USB3TN5 BC26

USB3TP5 AR29

USB3RN6 AN38

USB3RP6 AP29

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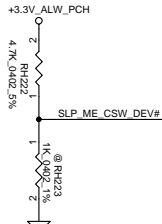
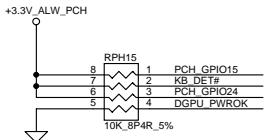
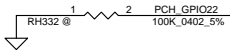
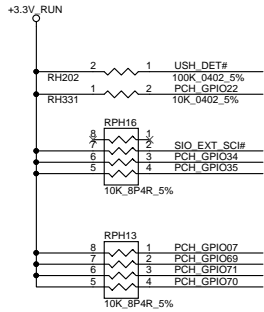
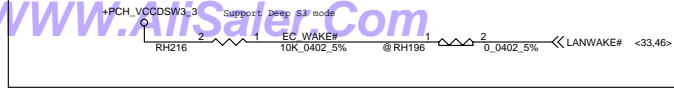
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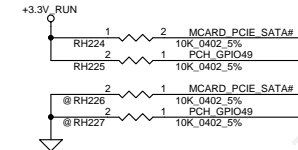
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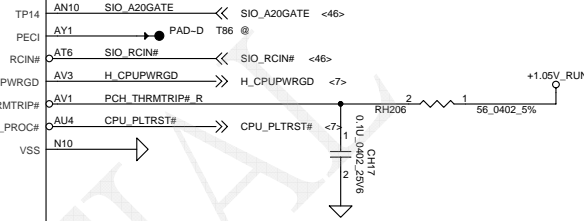
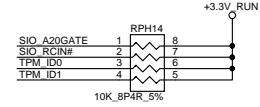
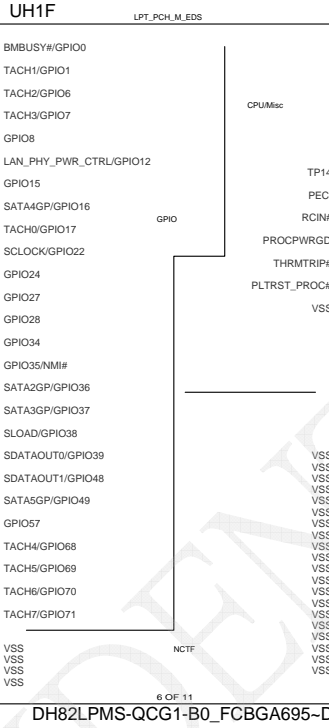
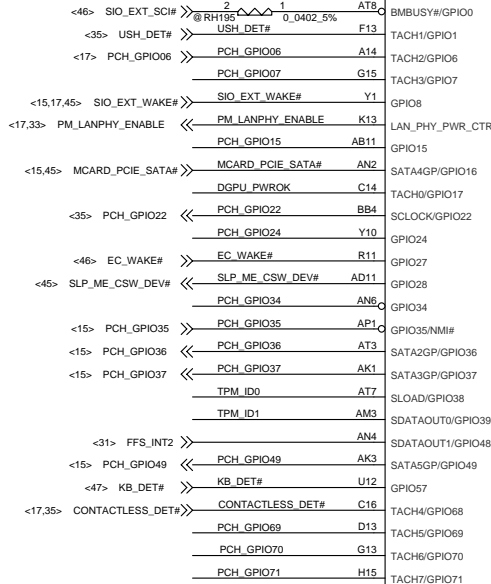
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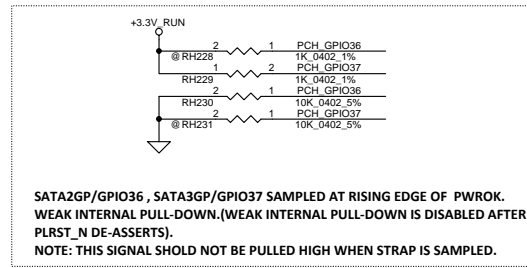
PLL ON DIE VR ENABLE
ENABLED - HIGH(DEFAULT)
DISABLED - LOW



Config	GPIO16,49
USB X4,PCIEX8,SATAx6	11
USB X6,PCIEX8,SATAx4	01



CRB1.2 already change to GND directly at UH1.A44, B45, BD1 pins



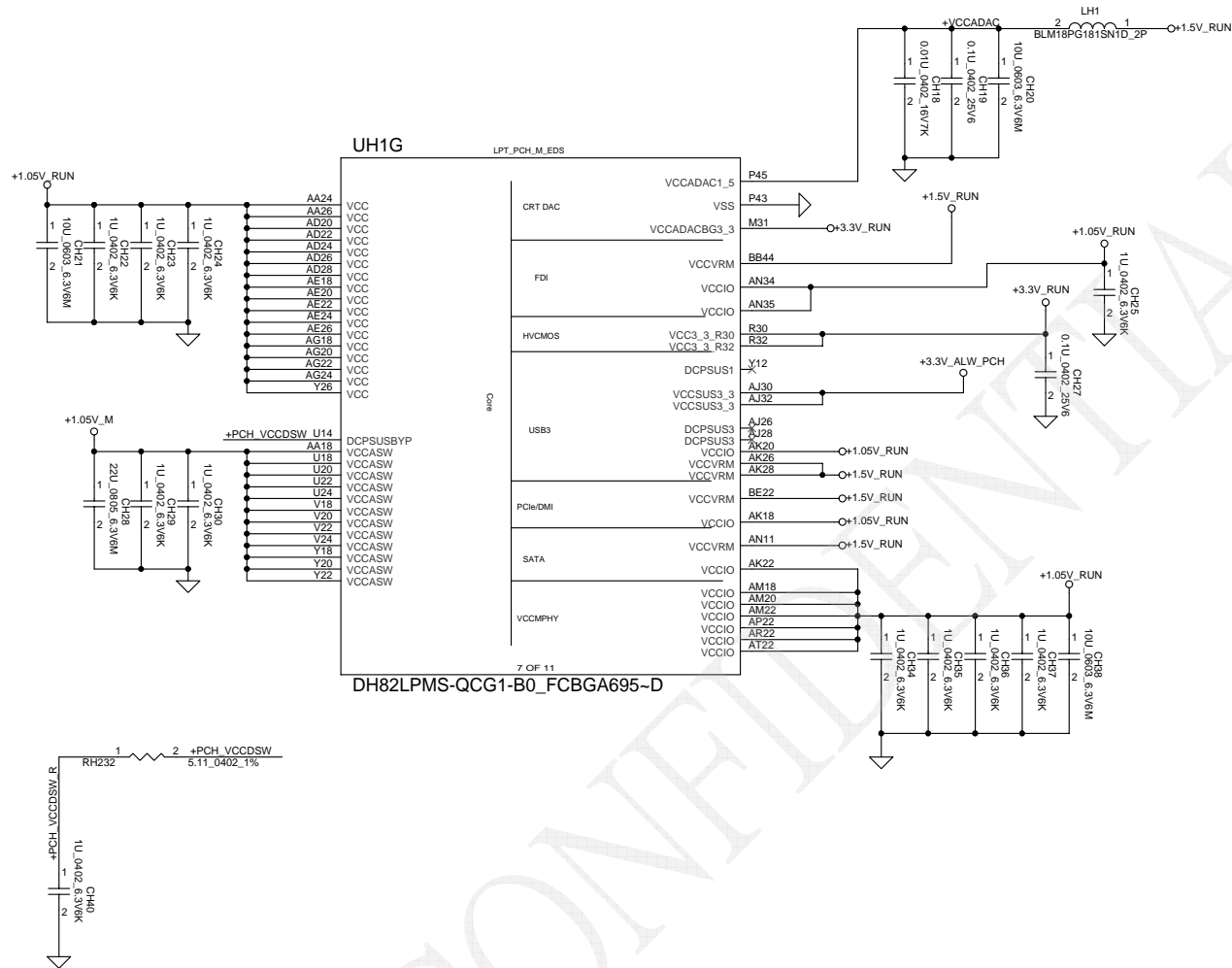
Fixed Signals				Muxed Signals		Fixed Signals						Muxed Signals		Fixed Signals					
USB3 1	USB3 2	USB3 5	USB3 6	PCIE 1	PCIE 2	PCIE 3	PCIE 4	PCIE 5	PCIE 6	PCIE 7	PCIE 8	SATA 4	SATA 5	SATA 0	SATA 1	SATA 2	SATA 3		
				(00)	(00)							(00)	(00)						
				USB3 3	USB3 4							PCIE 1	PCIE 2						
				(01)	(01)							(01)	(01)						

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Size	Document Number	4019MV		B
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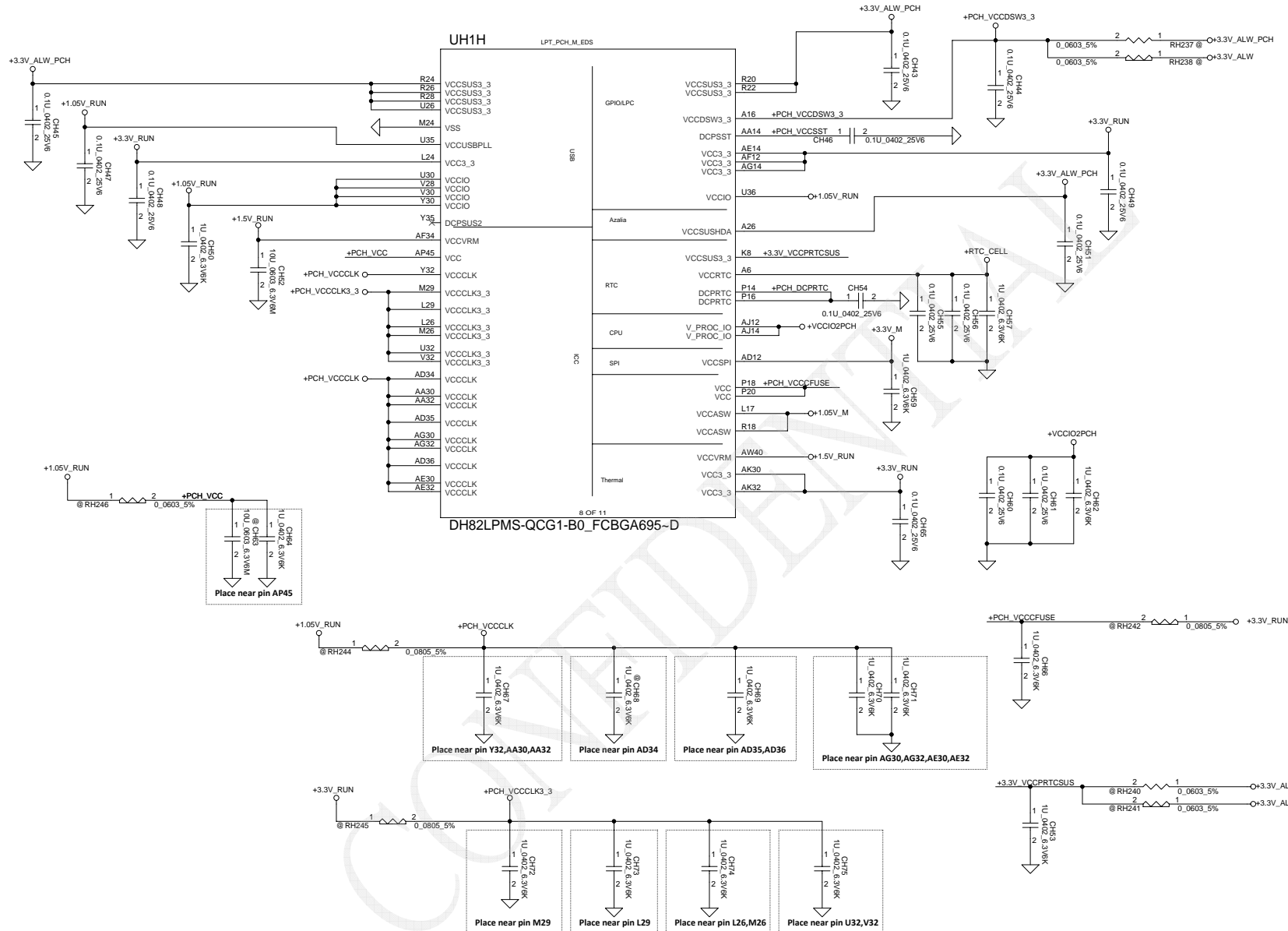
PCH Power Rail Table		
Voltage Rail	Voltage	SO Iccmax Current (A)
VCC	1.05V	1.138 A
VCCIO	1.05V	3.629 A
VCCADAC1_5	1.5V	0.070 A
VCCADAC3_3	3.3V	0.0133 A
VCCCLK	1.05V	0.306 A
VCCCLK3_3	3.3V	0.055 A
VCCVRM	1.5V	0.183 A
VCC3_3	3.3V	0.133 A
VCCASW	1.05V	0.67 A
VCCSUSHDA	3.3V	0.01 A
VCCSPI	3.3V	0.022 A
VCCSUS3_3	3.3V	0.261 A
VCCDSW3_3	3.3V	0.015 A
V_PROC_IO	1.05V	0.004 A

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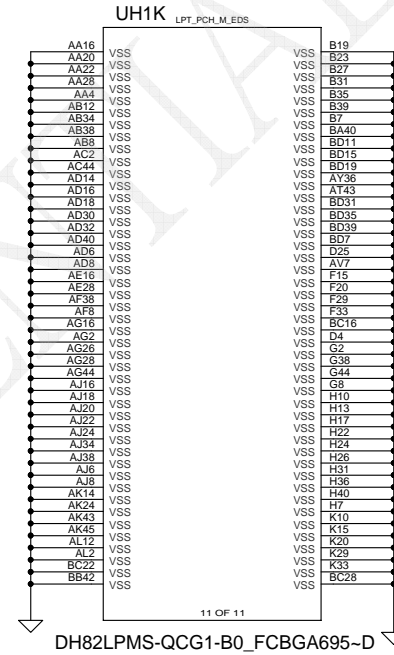
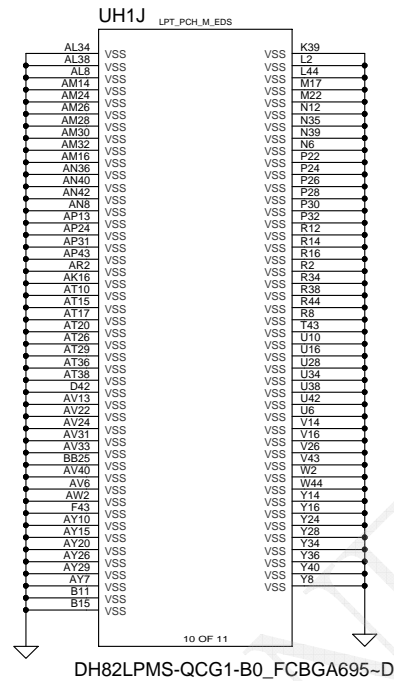
PCH Power Rail Table		
Voltage Rail	Voltage	S0 Iccmax Current (A)
VCC	1.05V	1.138 A
VCCIO	1.05V	3.629 A
VCCDAC1_5	1.5V	0.070 A
VCCDAC3_3	3.3V	0.0133 A
VCCCLK	1.05V	0.306 A
VCCCLK_3	3.3V	0.055 A
VCCVRM	1.5V	0.183 A
VCC3_3	3.3V	0.133 A
VCCASW	1.05V	0.67 A
VCCSUSHD	3.3V	0.01 A
VCCSPI	3.3V	0.022 A
VCCSUS3_3	3.3V	0.261 A
VCCDSW3_3	3.3V	0.015 A
V_PROC_IO	1.05V	0.004 A

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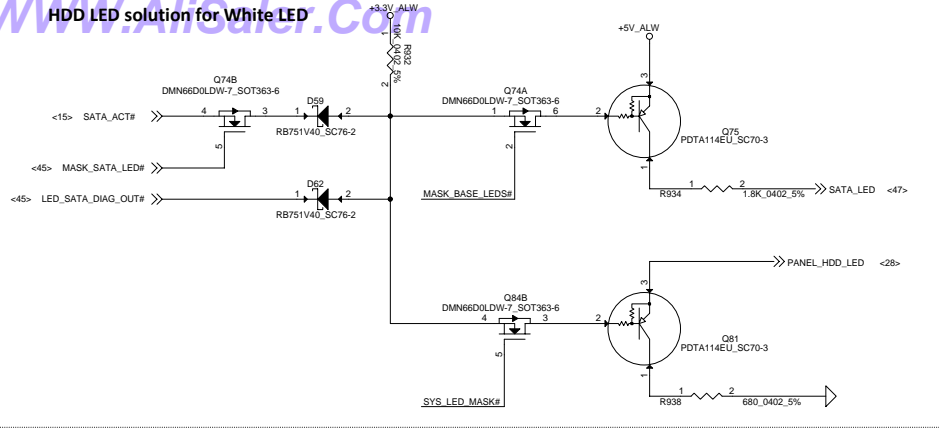
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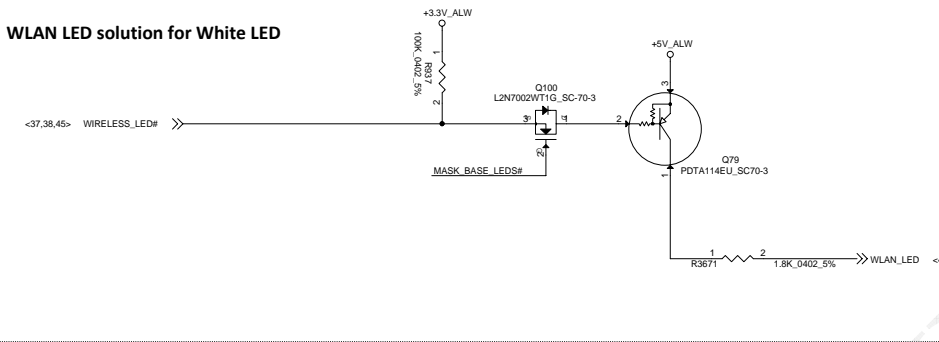
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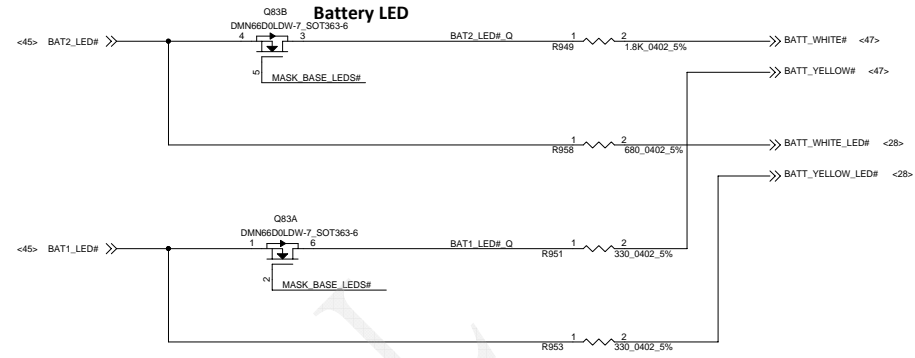
HDD LED solution for White LED



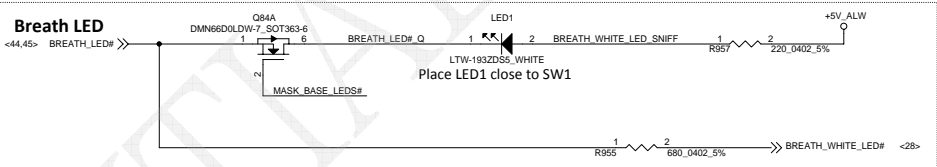
WLAN LED solution for White LED



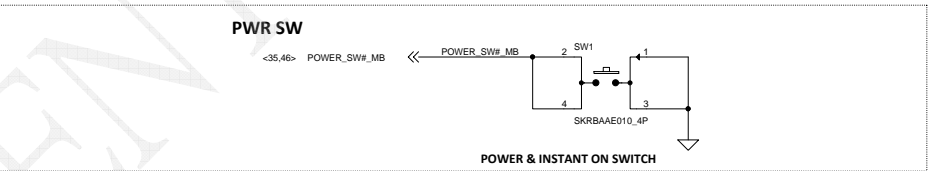
Battery LED



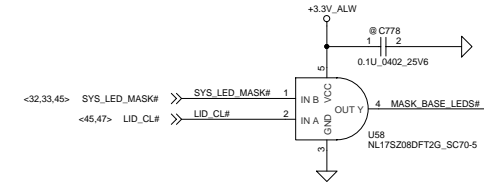
Breath LED



PWR SW

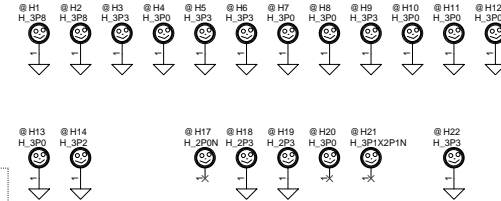


POWER & INSTANT ON SWITCH



LED Circuit Control Table

	SYS_LED_MASK#	LID_CL#
Mask All LEDs (Sniffer Function)	0	X
Mask Base MB LEDs (Lid Closed)	1	0
Do not Mask LEDs (Lid Opened)	1	1



Fiducial Mark

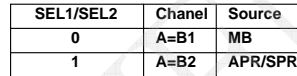


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SW for MB/DOCK

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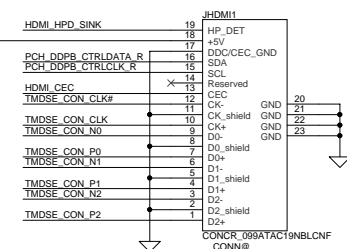
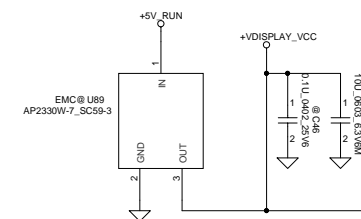
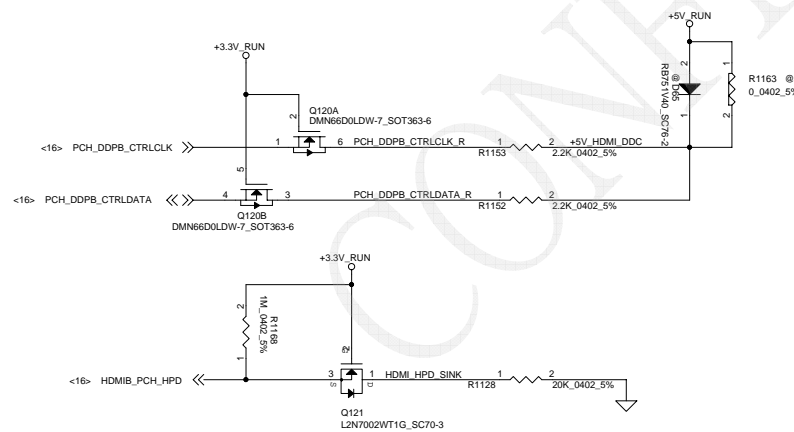
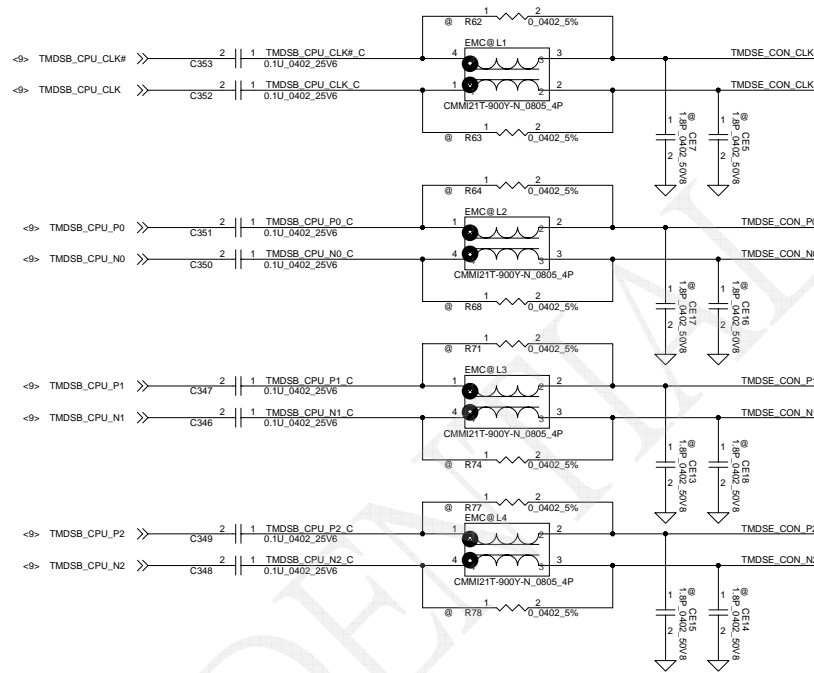
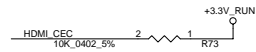
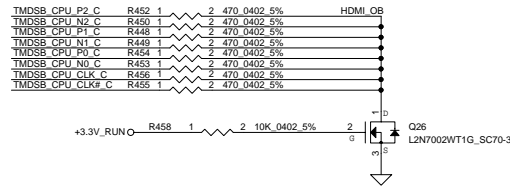
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4019MV

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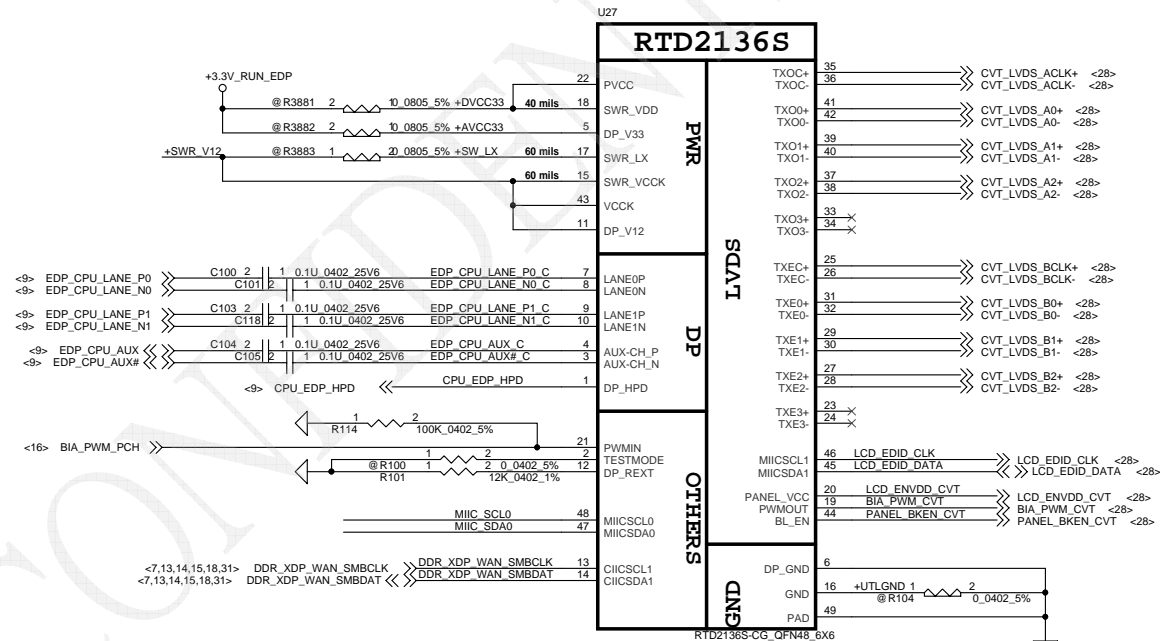
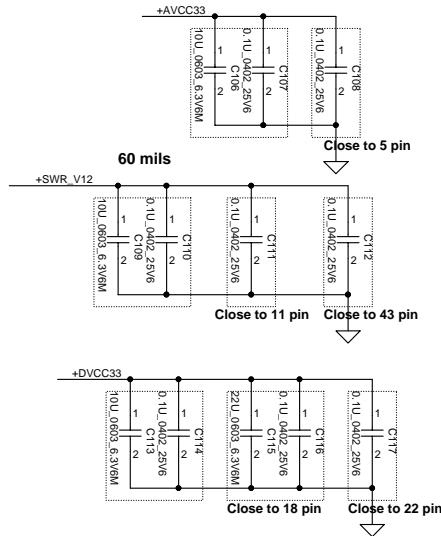
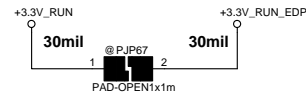
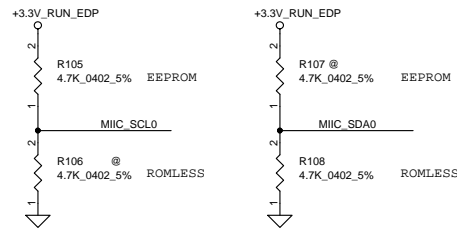
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Schematic, MB LA-9931P

4019MV

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Power Consumption:

- Pin5 (DPV33) < 20mA
- Pin 11 (DPV12) < 100mA
- Pin 15 (SWR_VCCK) < 100mA (layout trace > 60 mil)
- Pin 17 (SWR_LX) < 600mA (layout trace > 60 mil)
- Pin 18 (SWR_VDD) < 200mA (layout trace > 40 mil)
- Pin 22 (PVCC) < 50 mA
- Pin 43 (VCCK) < 50mA

Symbol need to update

1. RTD2136S:SA00004NW10 Populated R102, R103, R107; De-populated R108
- *** 2. RTD2136R SA000067100 : De-Populated R102, R103, R107; Populate R108;

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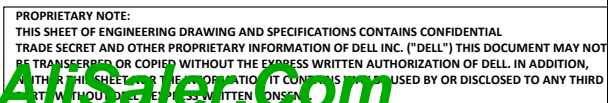
SCHEMATIC.MB LA-9931P

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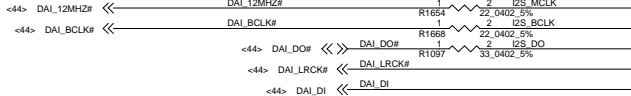
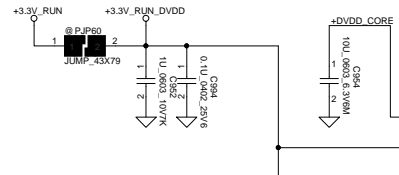
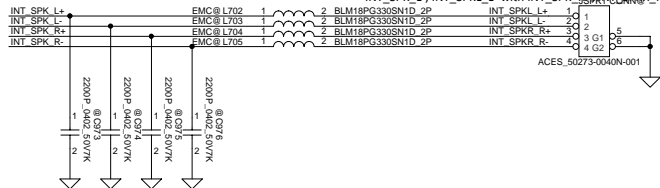
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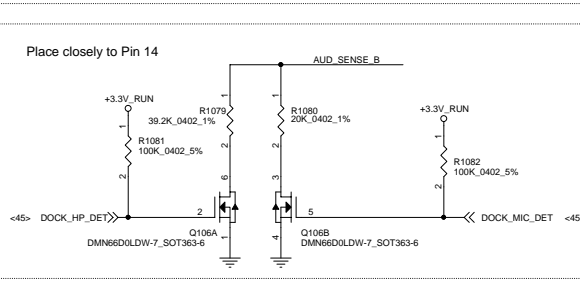
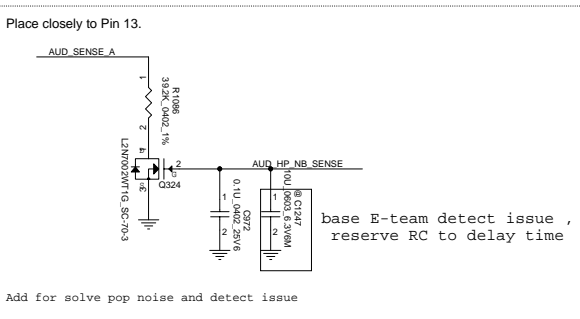
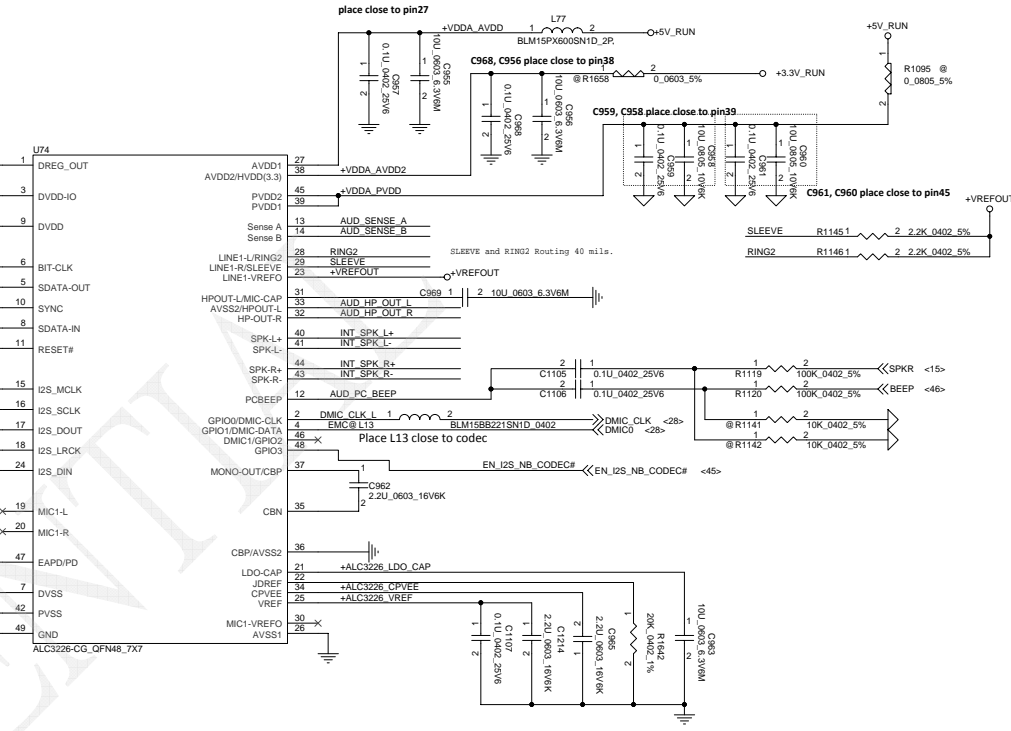
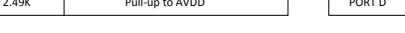
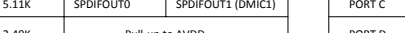
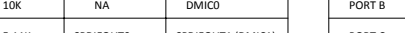
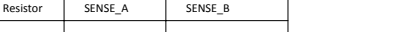
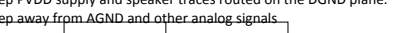
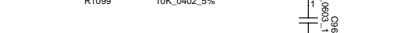
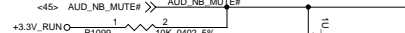
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INT_SPK_L+, INT_SPKL_L+ :40 mils trace, keep 10mils spacing
INT_SPK_L-, INT_SPKL_L- :40 mils trace, keep 10mils spacing
INT_SPK_R+, INT_SPKR_R+ :40 mils trace, keep 10mils spacing
INT_SPK_R-, INT_SPKR_R- :40 mils trace, keep 10mils spacing
INT_SPK_L-, INT_SPKL_L- with INT_SPK_R-, INT_SPKR_R+ keep 20mils spacing



BCLK: Audio serial data bus bit clock input/output
LRCK: Audio serial data bus word clock input/output



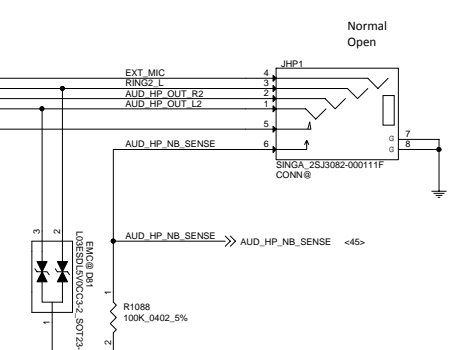
Notes:
Keep PVDD supply and speaker traces routed on the DGND plane.
Keep away from AGND and other analog signals

Resistor	SENSE_A	SENSE_B
39.2K	PORT A	PORT E
20K	PORT B	PORT F
10K	NA	DMIC0
5.11K	SPDIFOUT0	SPDIFOUT1 (DMIC1)
2.49K	Pull-up to AVDD	

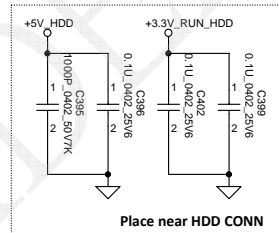
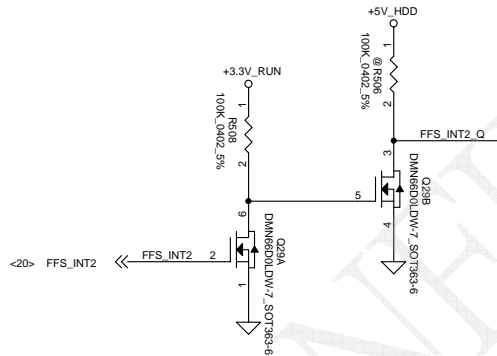
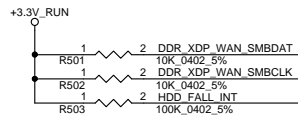
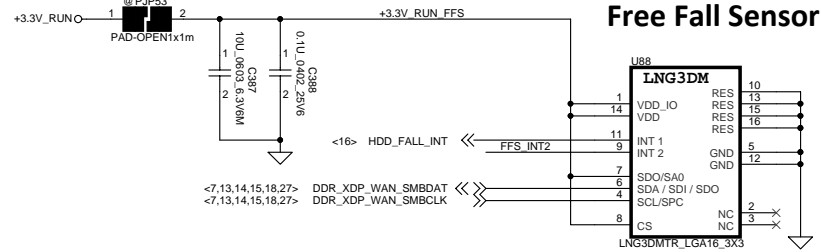
PORT A	External MIC
PORT B	HeadPhone Out
PORT C	Dock Audio
PORT D	Internal SPK

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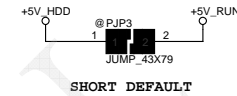
Combo Jack



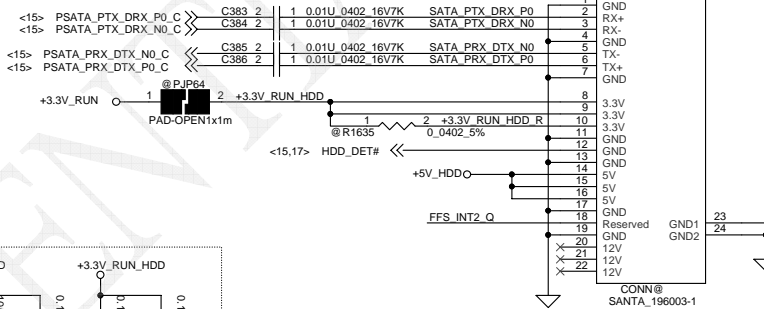
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HDD PWR



HDD CONN



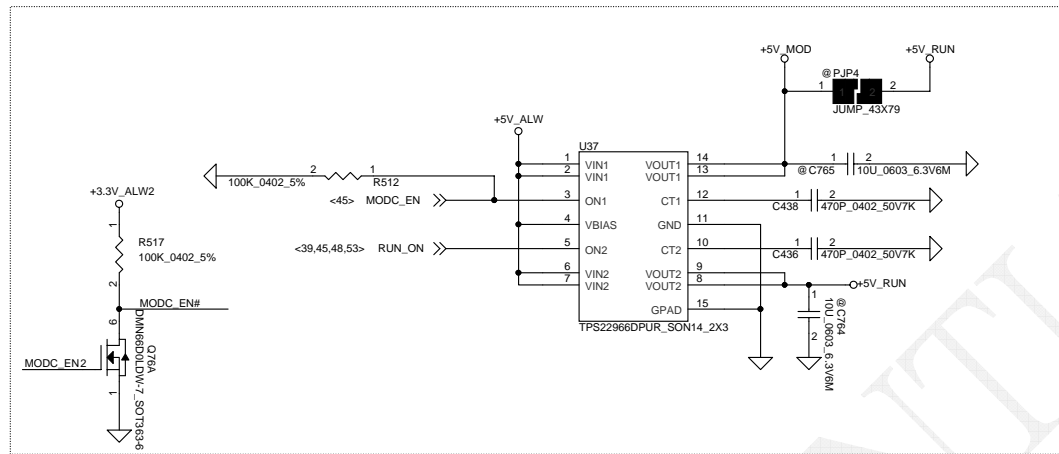
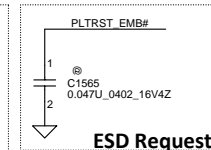
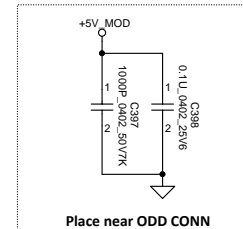
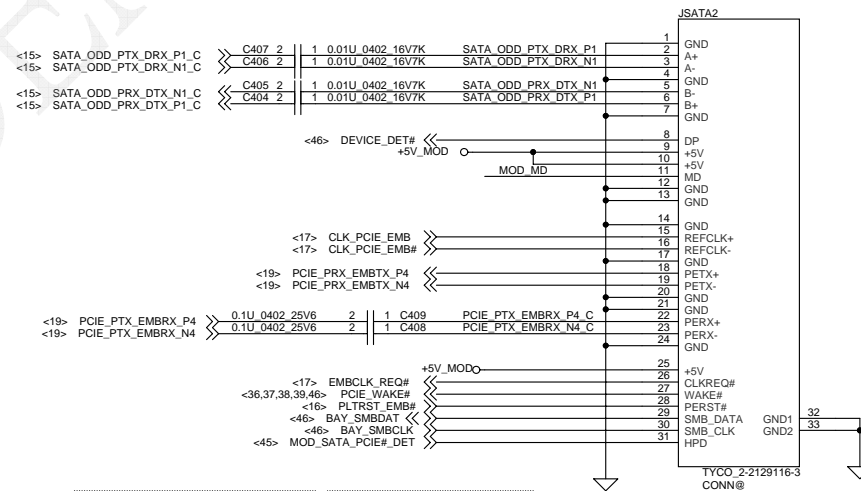
Main SATA +5V Default

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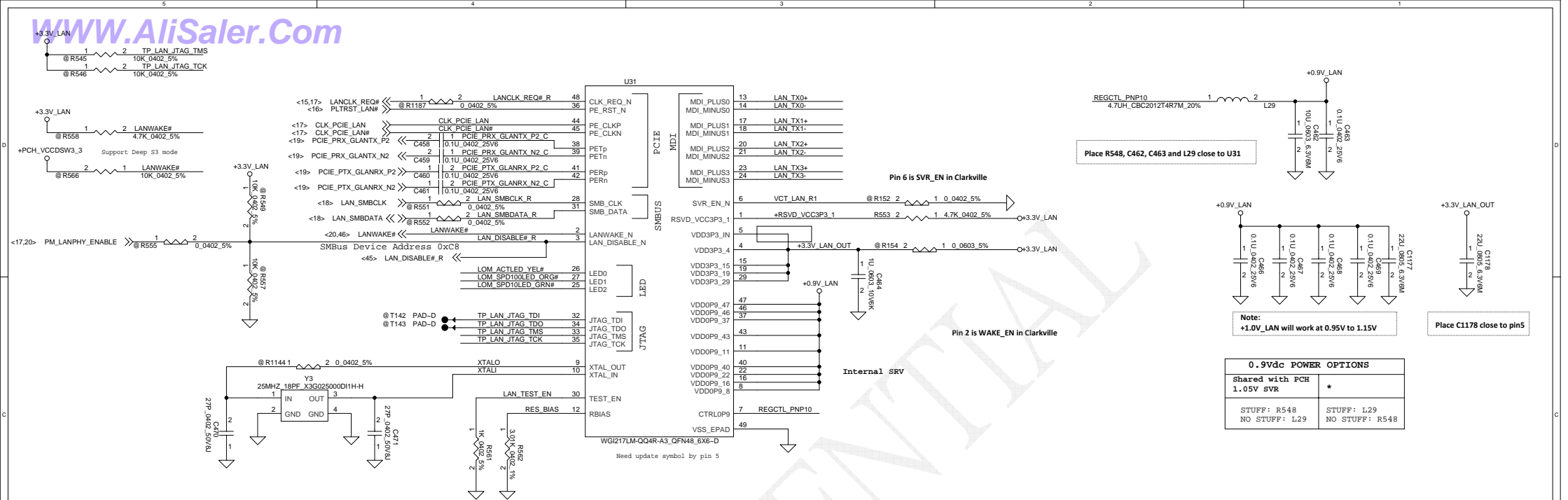
+5V_MOD/+5V_RUN source**ODD CONN**

Place near ODD CONN

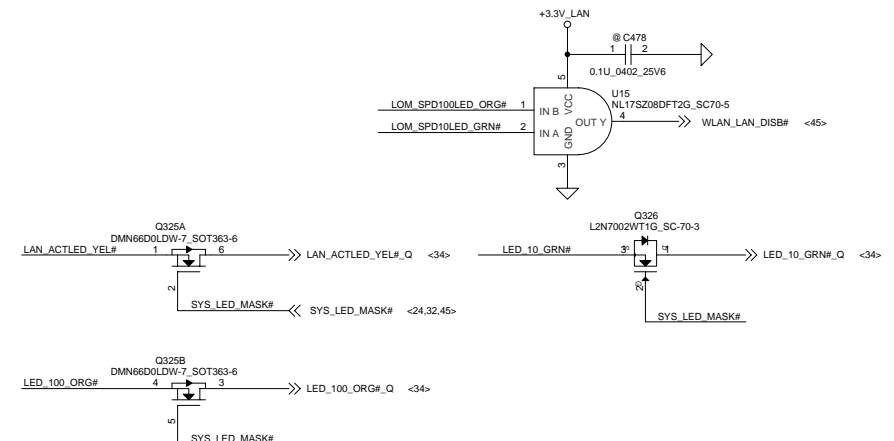
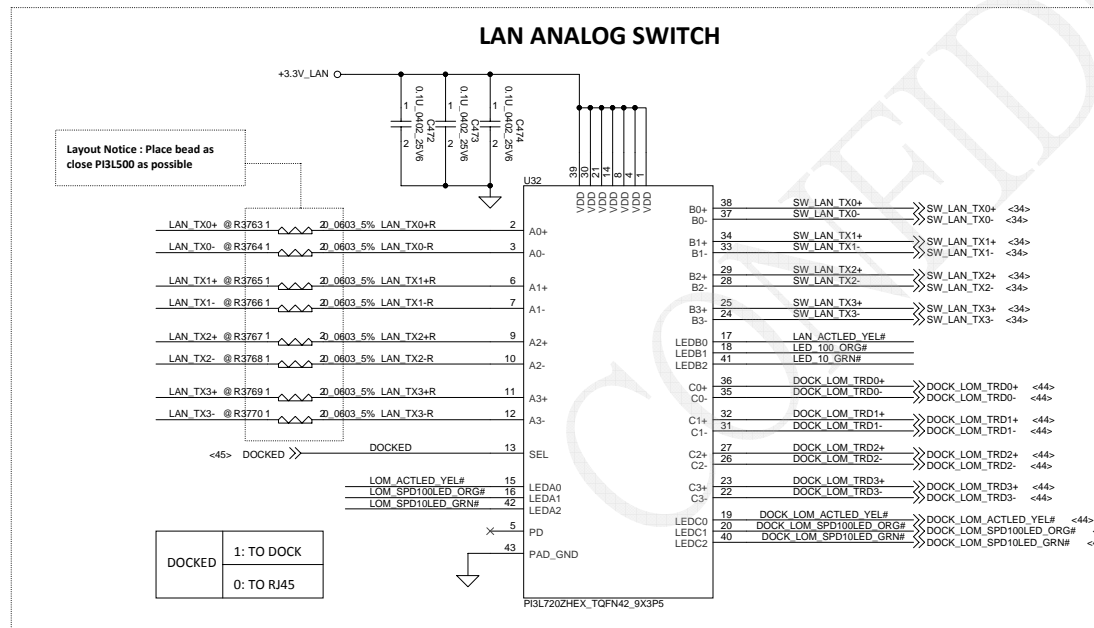
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LAN ANALOG SWITCH



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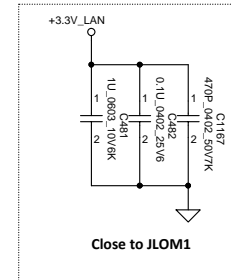
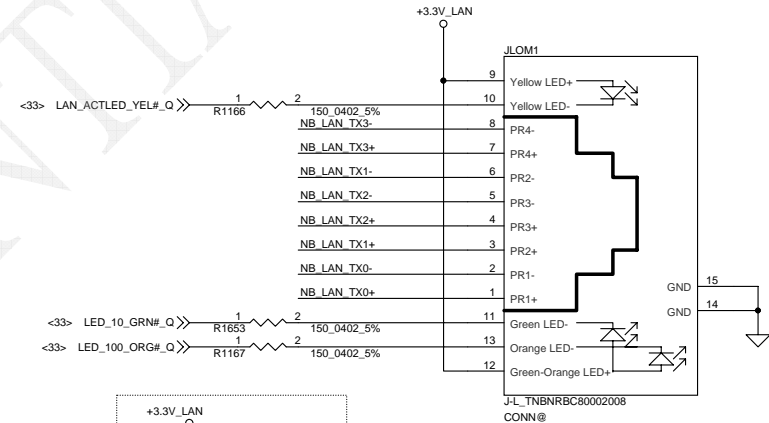
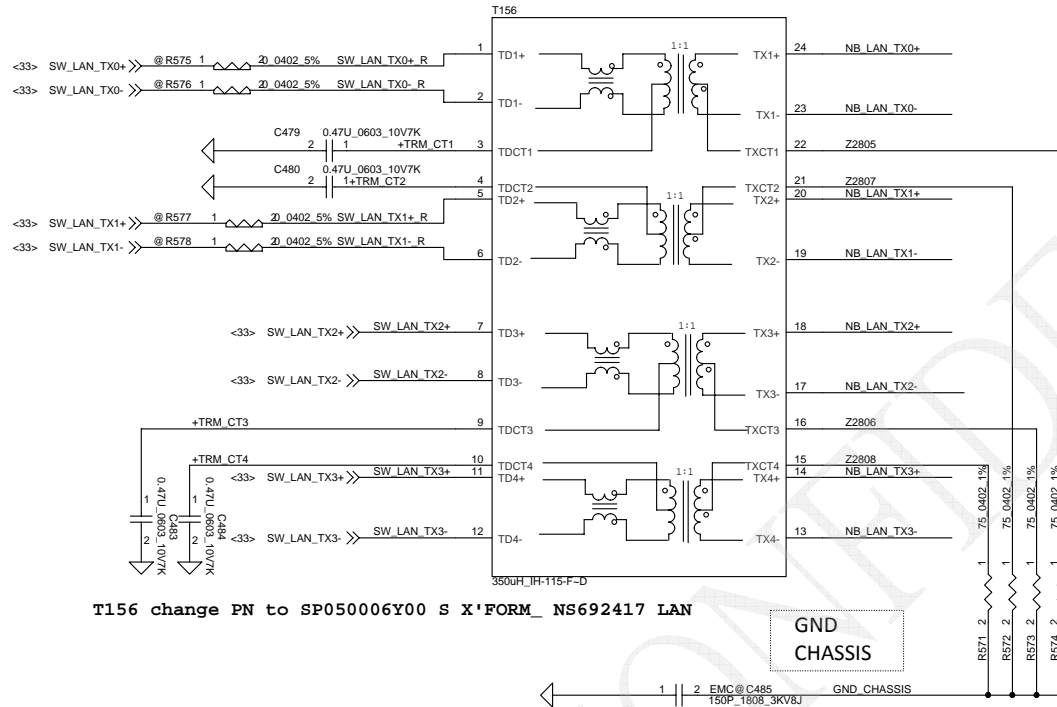
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SW_LAN_TX0+ R	1	2	SW_LAN_TX0- R
@ C500	1	2	10P_0402_50V8J
SW_LAN_TX1+ R	1	2	SW_LAN_TX1- R
@ C501	1	2	10P_0402_50V8J



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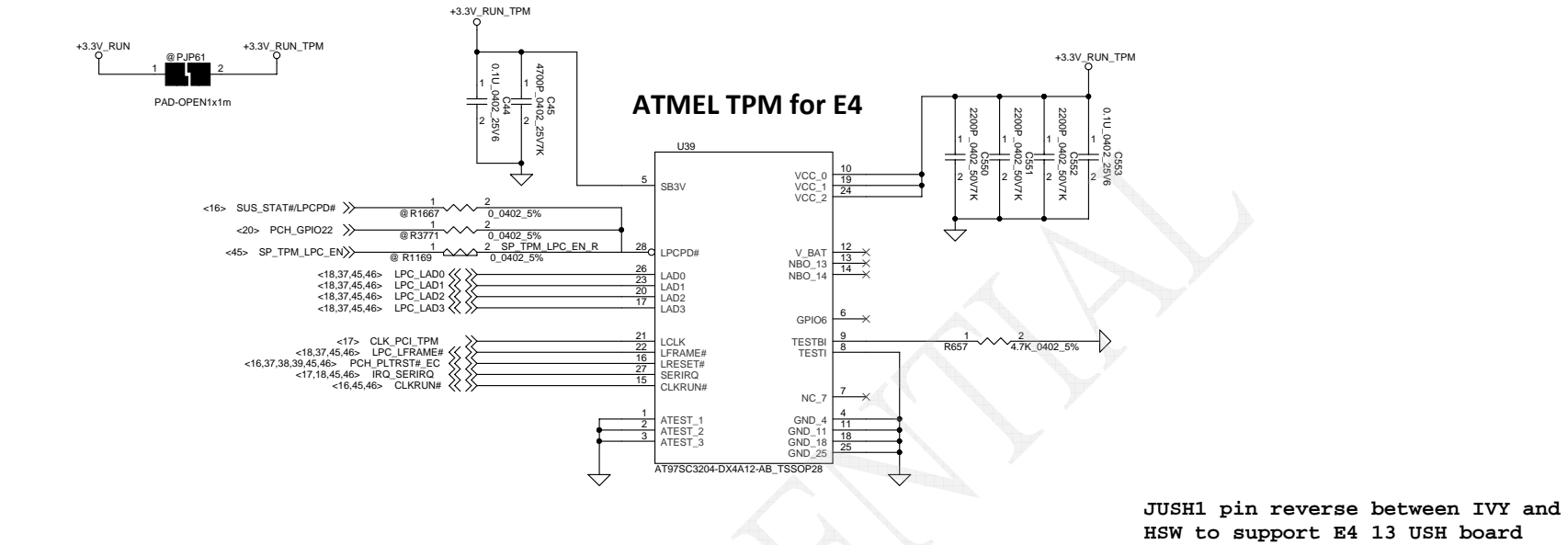
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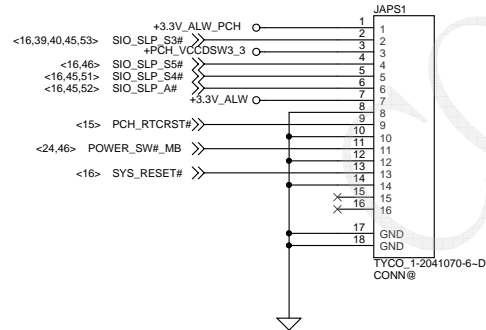
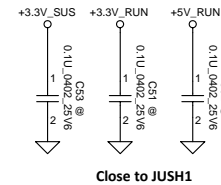
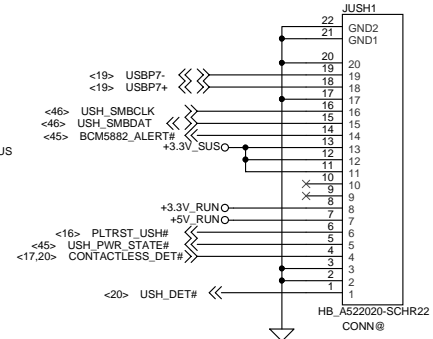
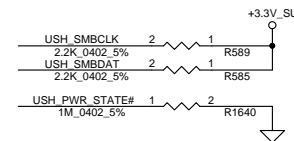
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USH CONN



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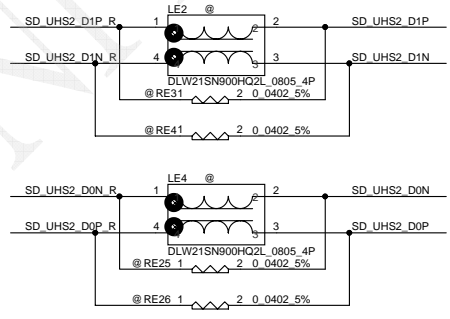
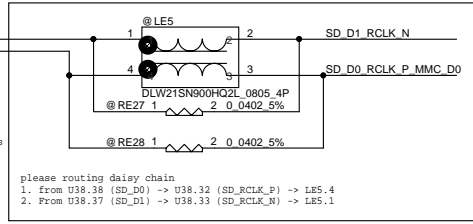
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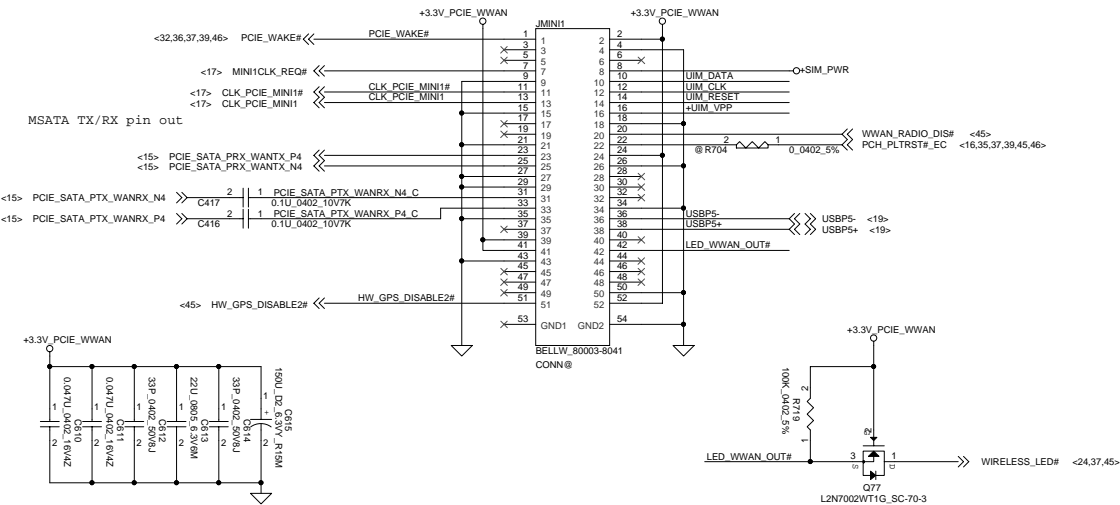
WWW.AliSaler.Com

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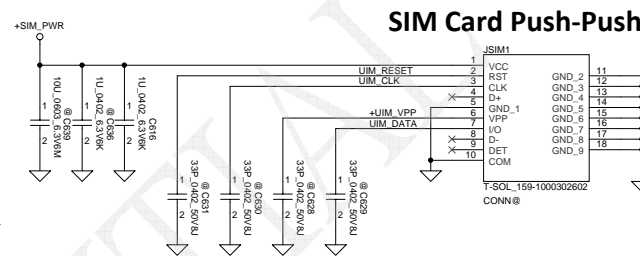
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(1) Wake Enabled and Active
(2) Wake NOT Enabled nor Active

PWR Rail	Voltage Tolerance	D0-D2&D3 hot Power		D3 cold Power	
		Peak(mA)	Typ(mA)	Peak(mA)	Typ(mA)
+3.3V_PCIE_WWAN	+/-9%	2750	800	800 (1)	150 (1) 5 (2)



SIM Card Push-Push

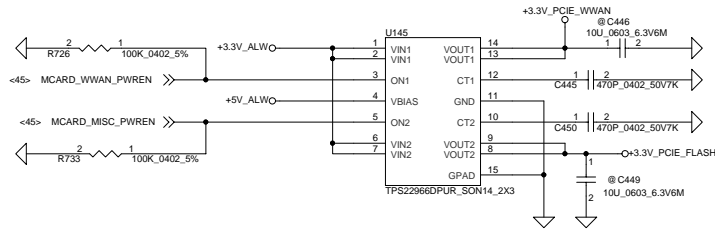
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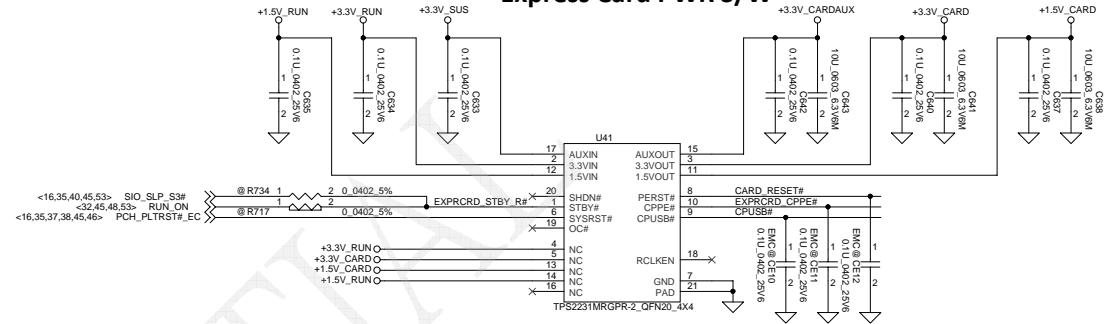
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+3.3V_PCIE_WWAN/+3.3V_PCIE_FLASH



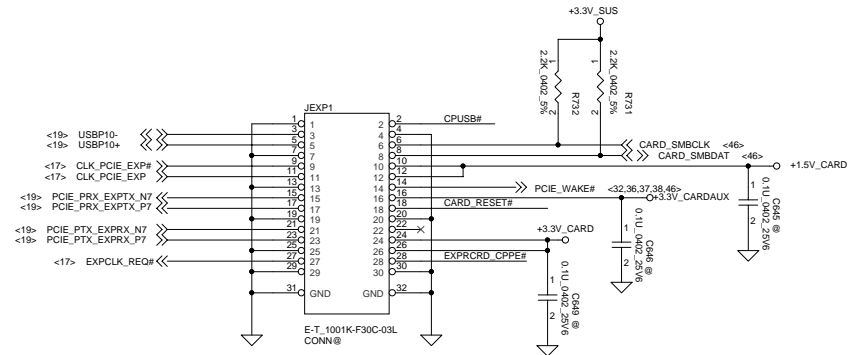
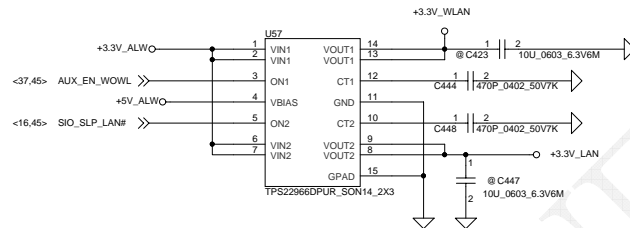
Express Card PWR S/W



Note: Add connection on pin4, pin5, pin 13 and pin14 to support GMT 2nd source part

Express Card Conn.

+3.3V_WLAN/+3.3V_LAN source



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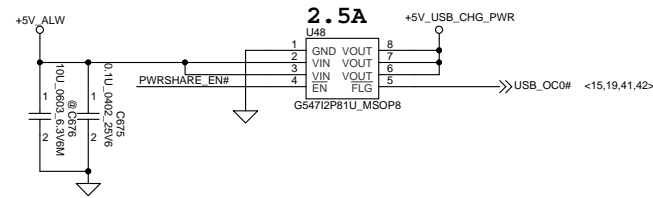
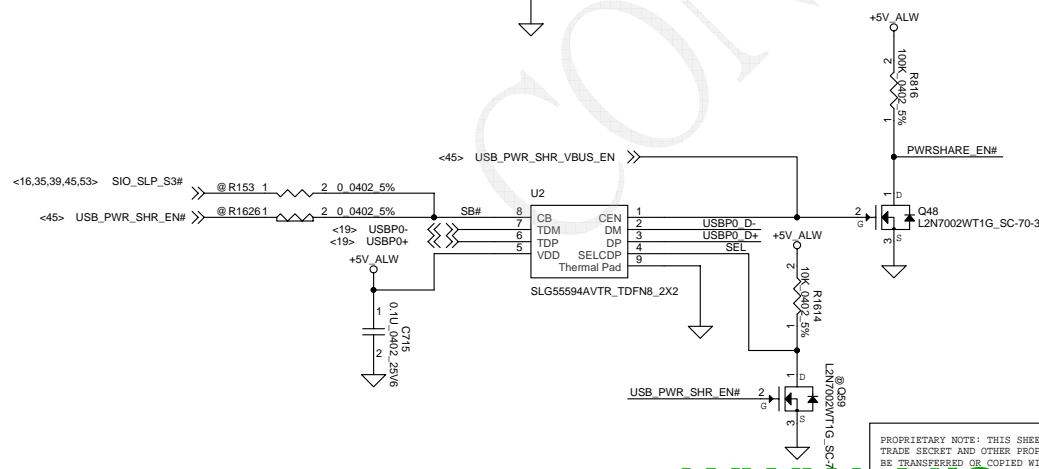
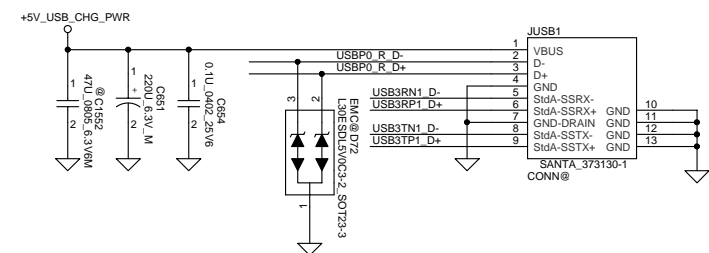
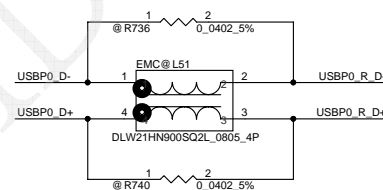
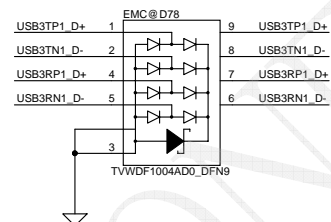
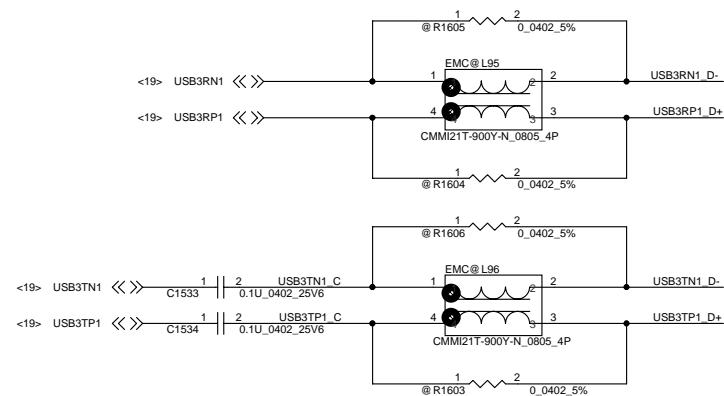
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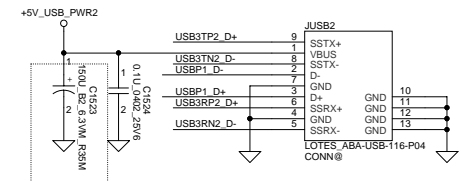
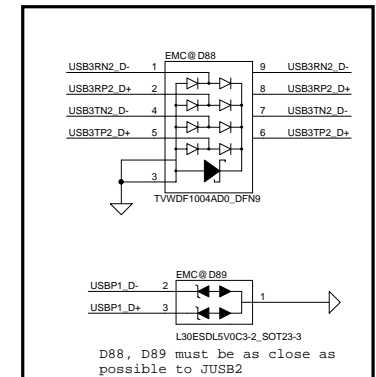
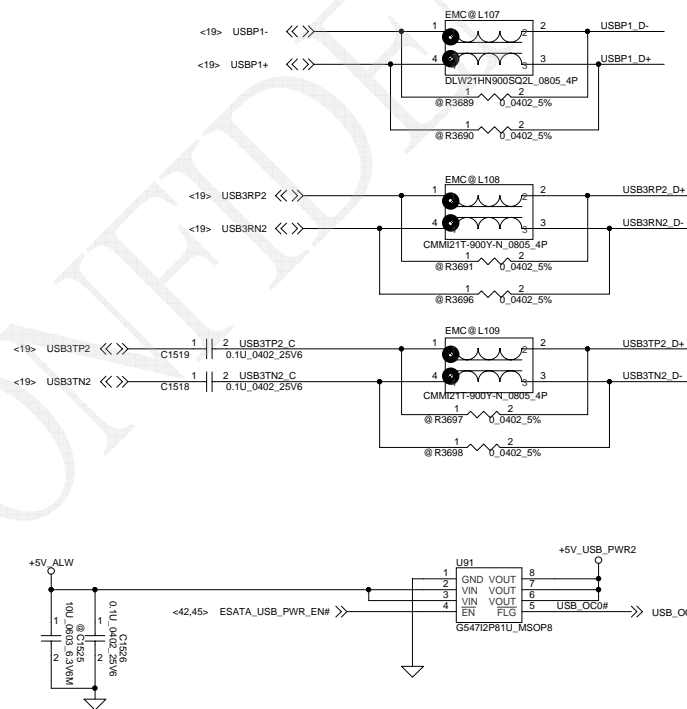
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place C1523 between JUSB2 and JUSB3

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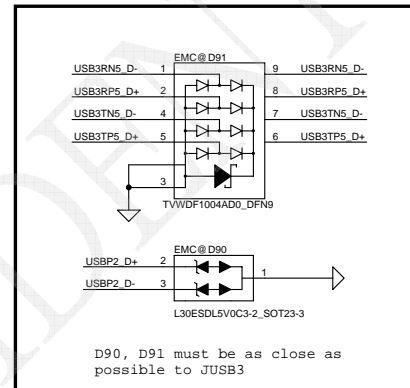
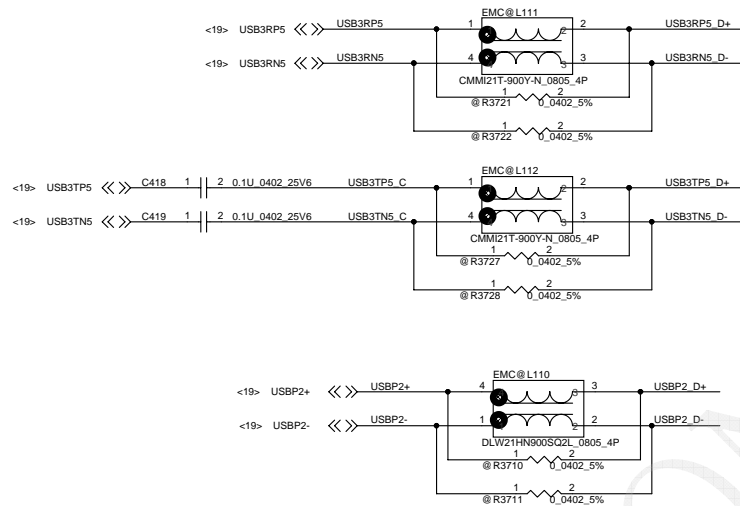
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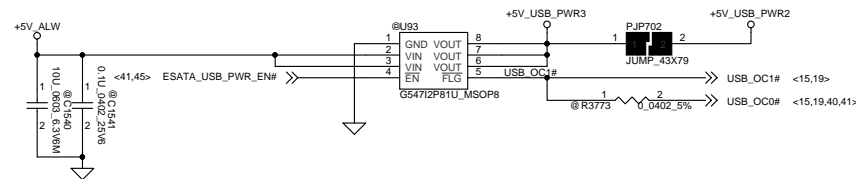
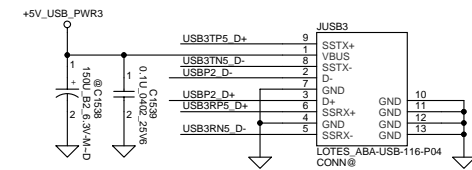
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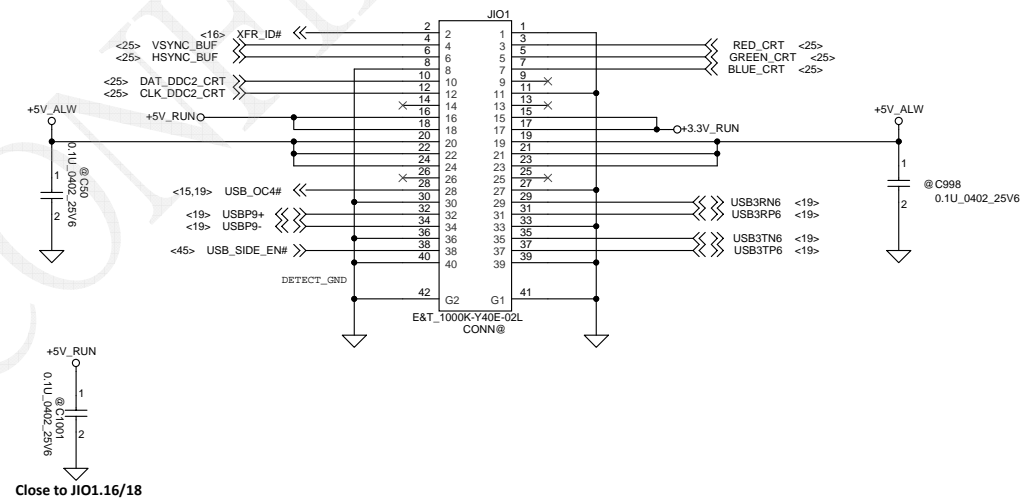
D90, D91 must be as close as possible to JUSB3



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I/O board CONN.



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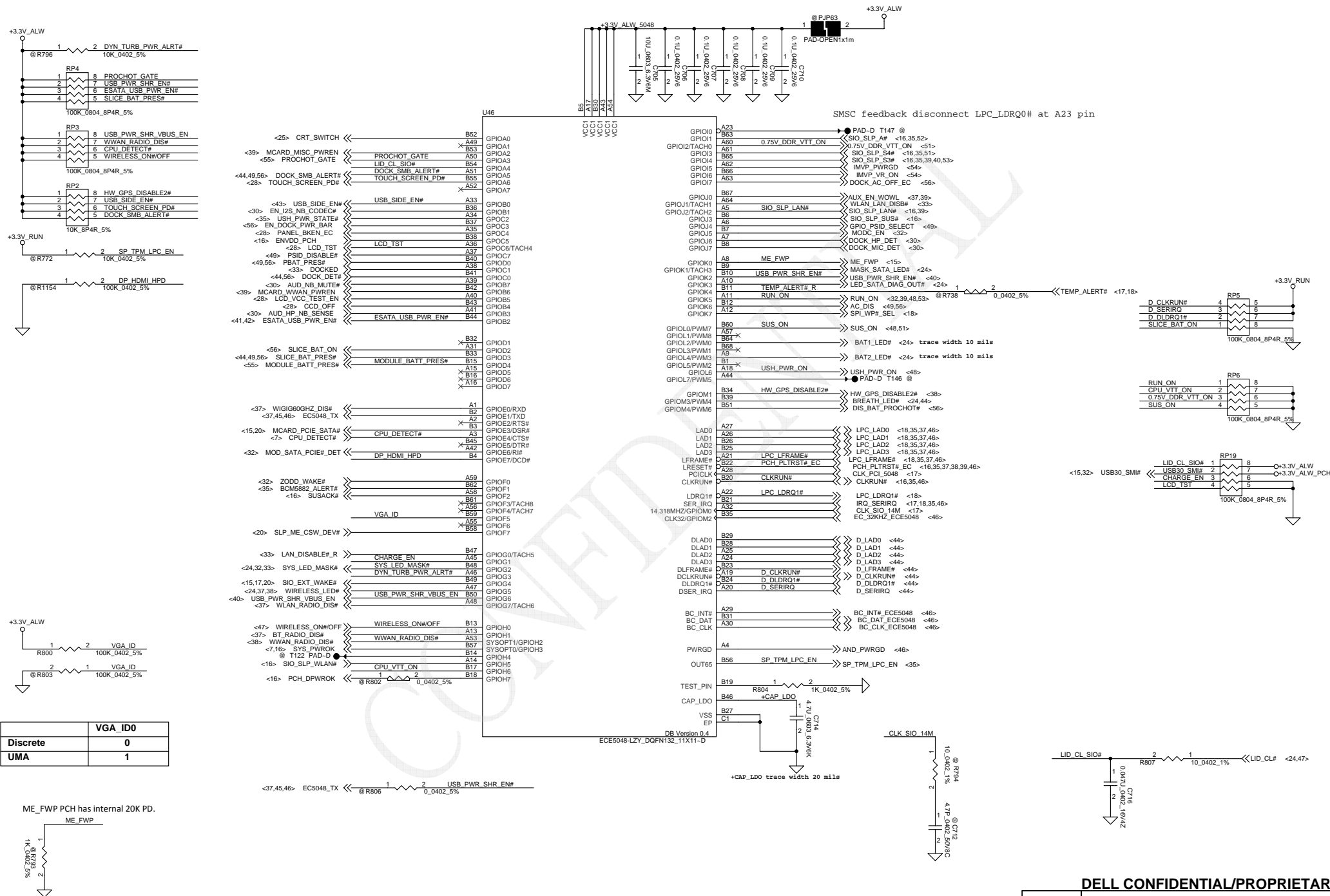
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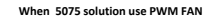
SCHEMATIC,MB LA-9931P

4019MV

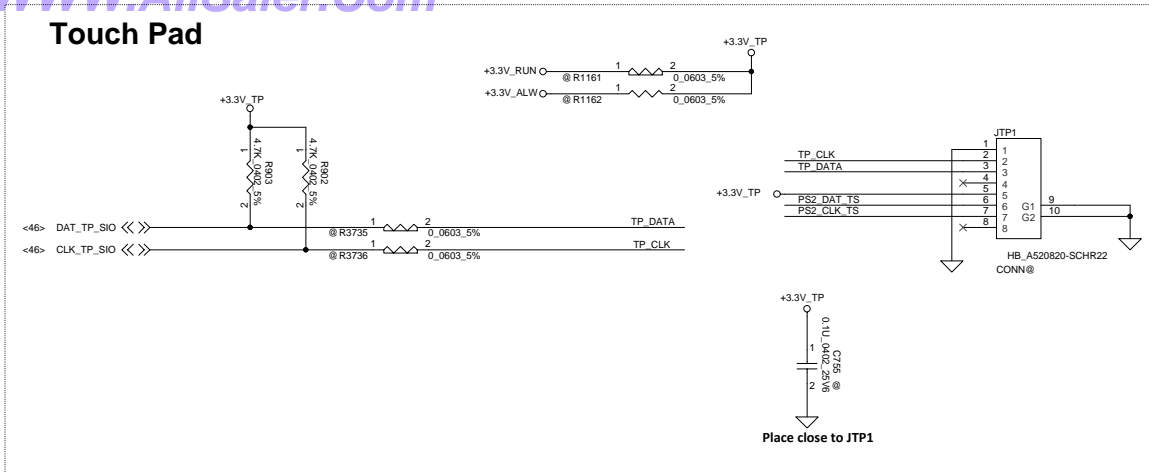
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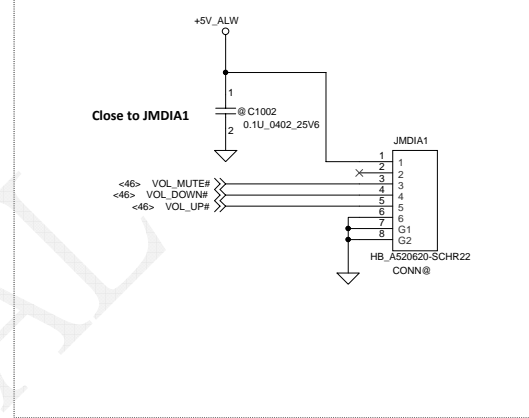
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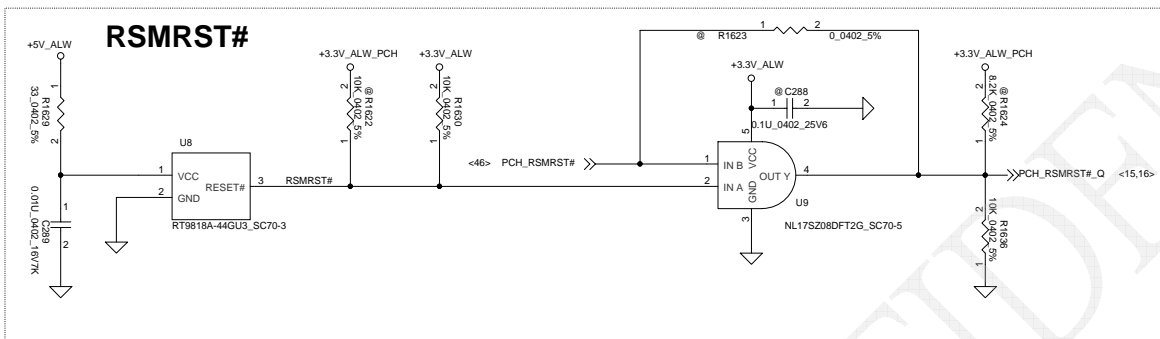
Touch Pad



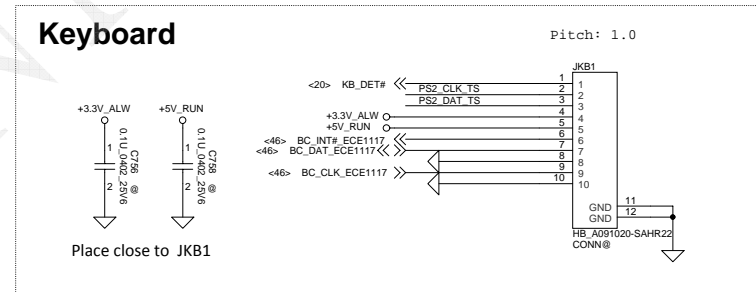
Media Board



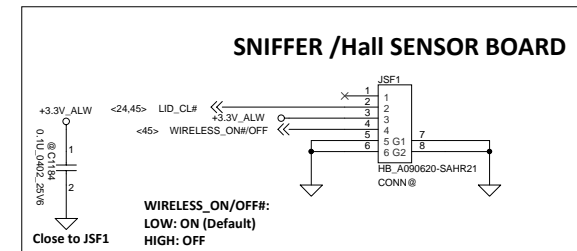
RSMRST#



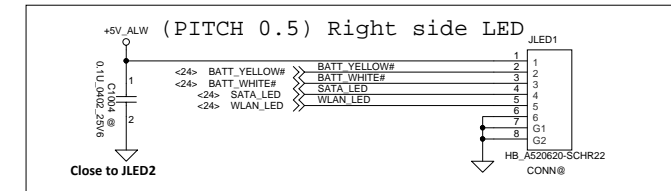
Keyboard



SNIFFER /Hall SENSOR BOARD



(PITCH 0.5) Right side LED

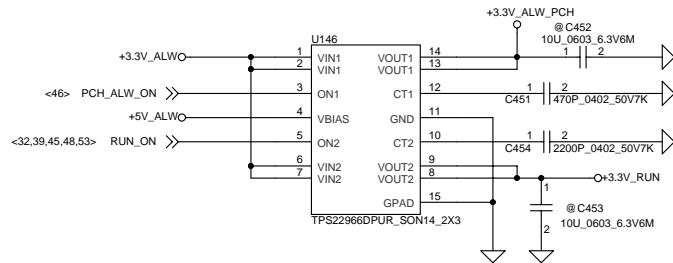


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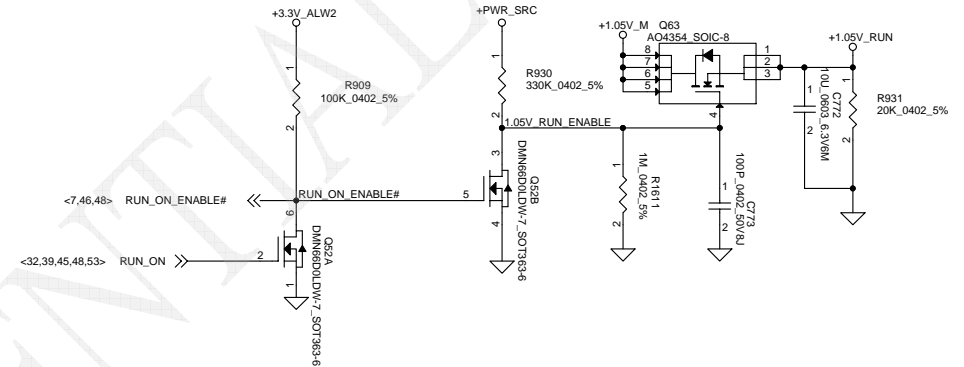
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+3.3V_ALW_PCH/+3.3V_RUN Source

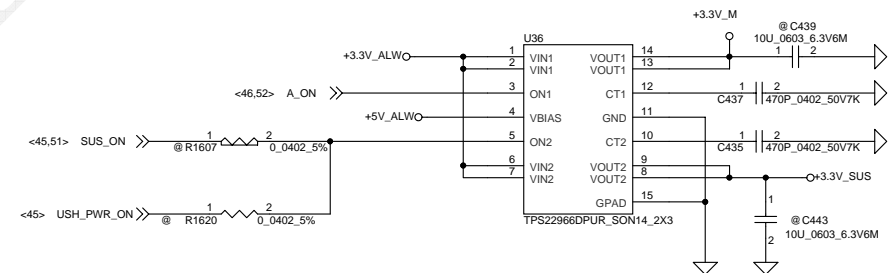


SI4164DY		
VDS	RDS(on)	ID(A)
30	0.0032ohm at VGS=10V	30A
30	0.0039ohm at VGS=4.5V	26.3A

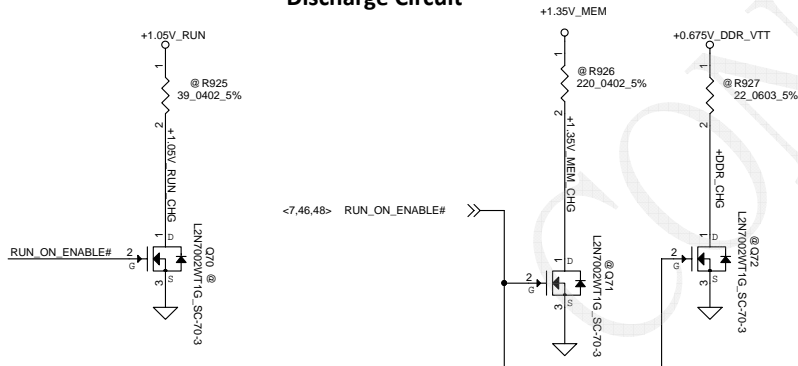
+1.05V_RUN Source



+3.3V_M/+3.3V_SUS source



Discharge Circuit



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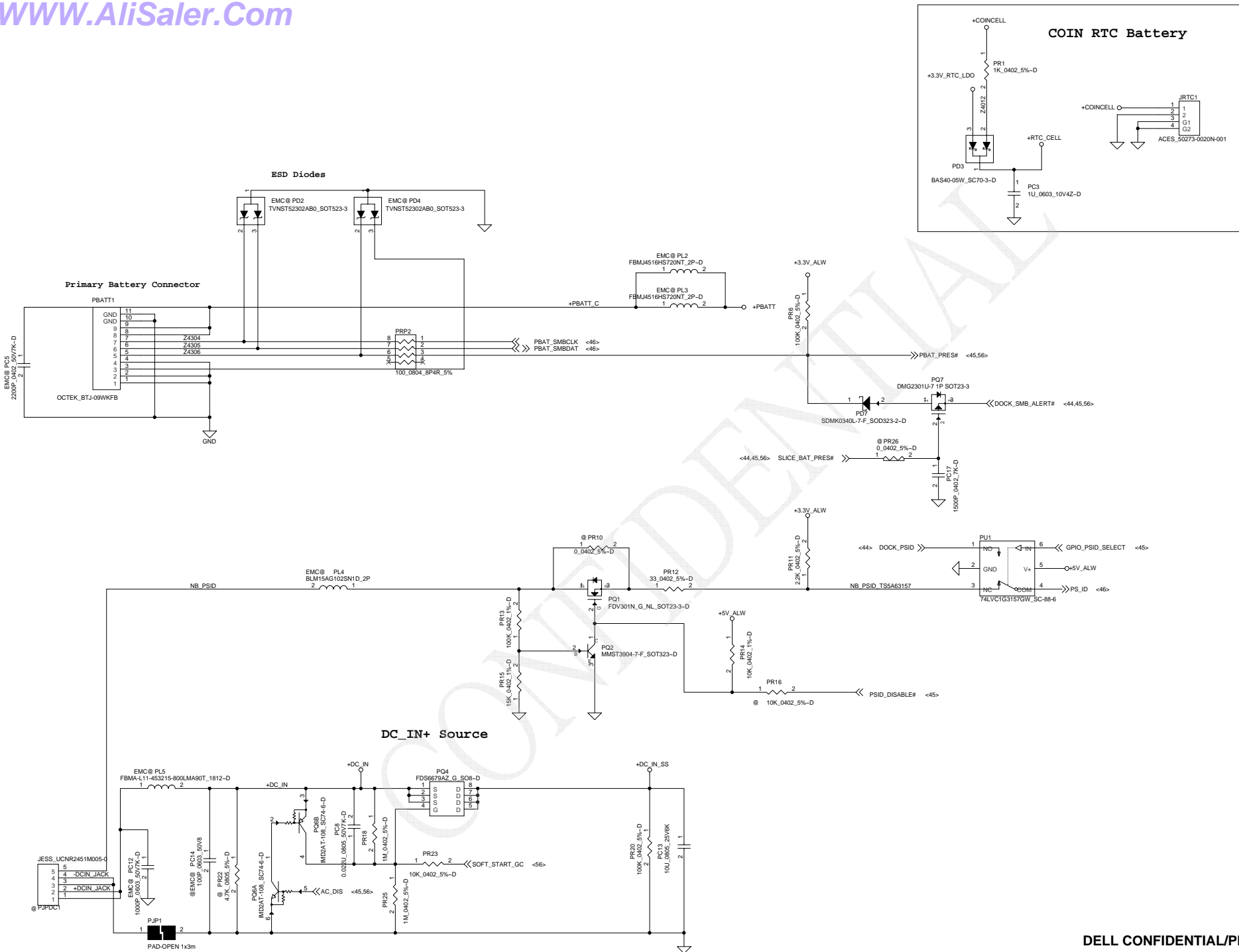
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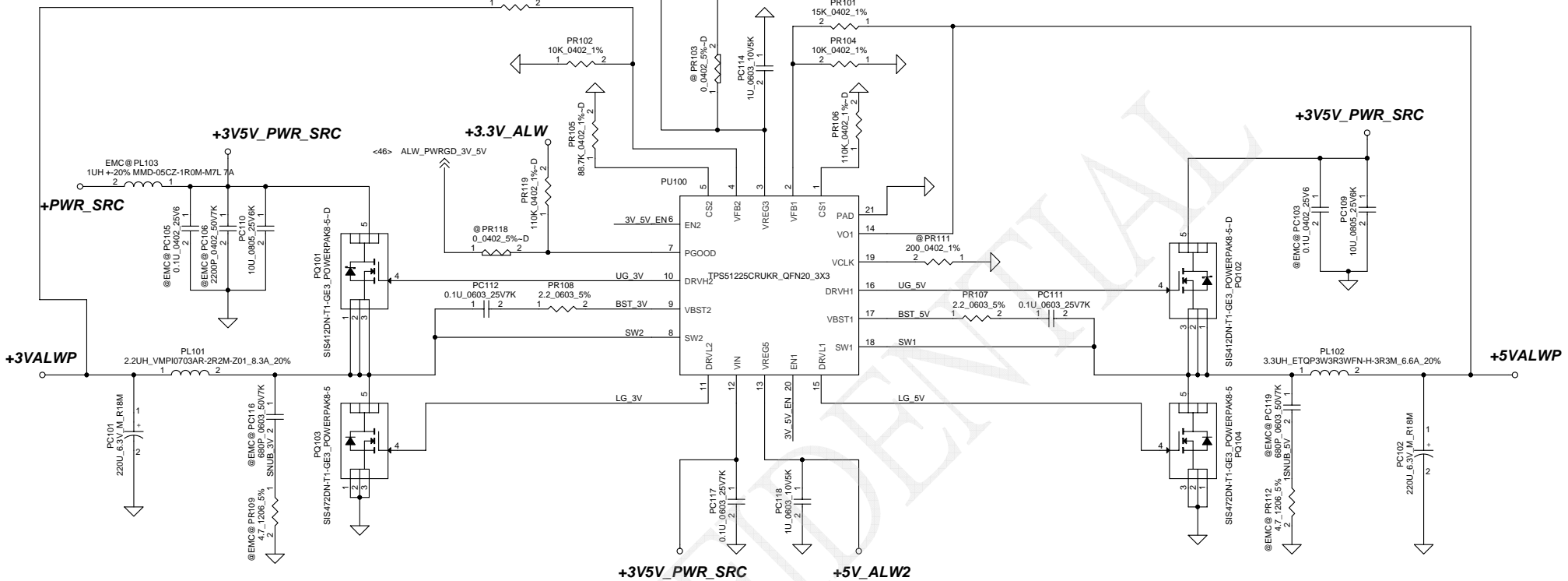
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$$VFB2=2V, PR100=(V_{out}-0.5*V_{ripple}-2)/2*PR102$$

+3.3V_ALW2

+3.3V_RTC_LDO

$$VFB1=2V, PR101=(V_{out}-0.5*V_{ripple}-2)/2*PR104$$

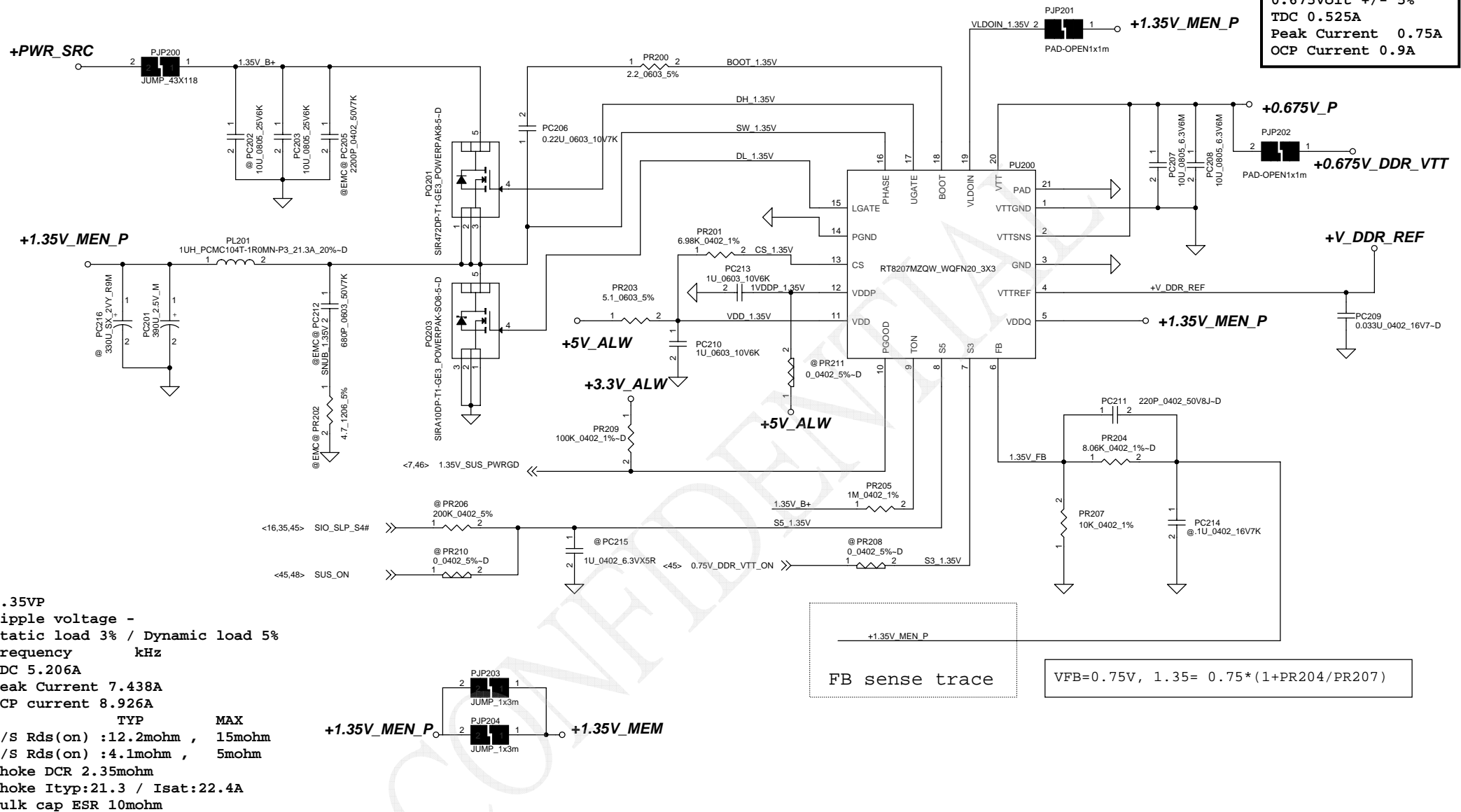


3VALWP
Ripple voltage -
Static load 3% / Dynamic load 5%
Frequency 350kHz
TDC 5.099A
Peak Current 7.285A
OCP current 8.742A
TYP MAX
H/S Rds(on) 24mohm , 30mohm
L/S Rds(on) 10.3mohm , 12.4mohm
Choke DCR Max:17mohm
Choke Ityp:8.3A / Isat:10.8A
Bulk cap ESR 18mohm



5VALWP
Ripple voltage -
Static load 3% / Dynamic load 5%
Frequency 300kHz
TDC 6.075A
Peak Current 8.679A
OCP current 10.415A
TYP MAX
H/S Rds(on) 24mohm , 30mohm
L/S Rds(on) 10.3mohm , 12.4mohm
Choke DCR Max:28mohm
Choke Ityp:6.6A / Isat:8.2A
Bulk cap ESR 18mohm

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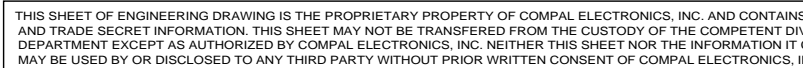
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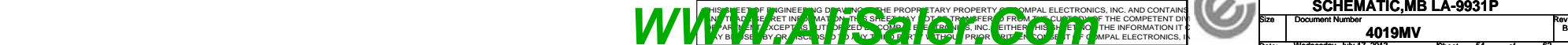
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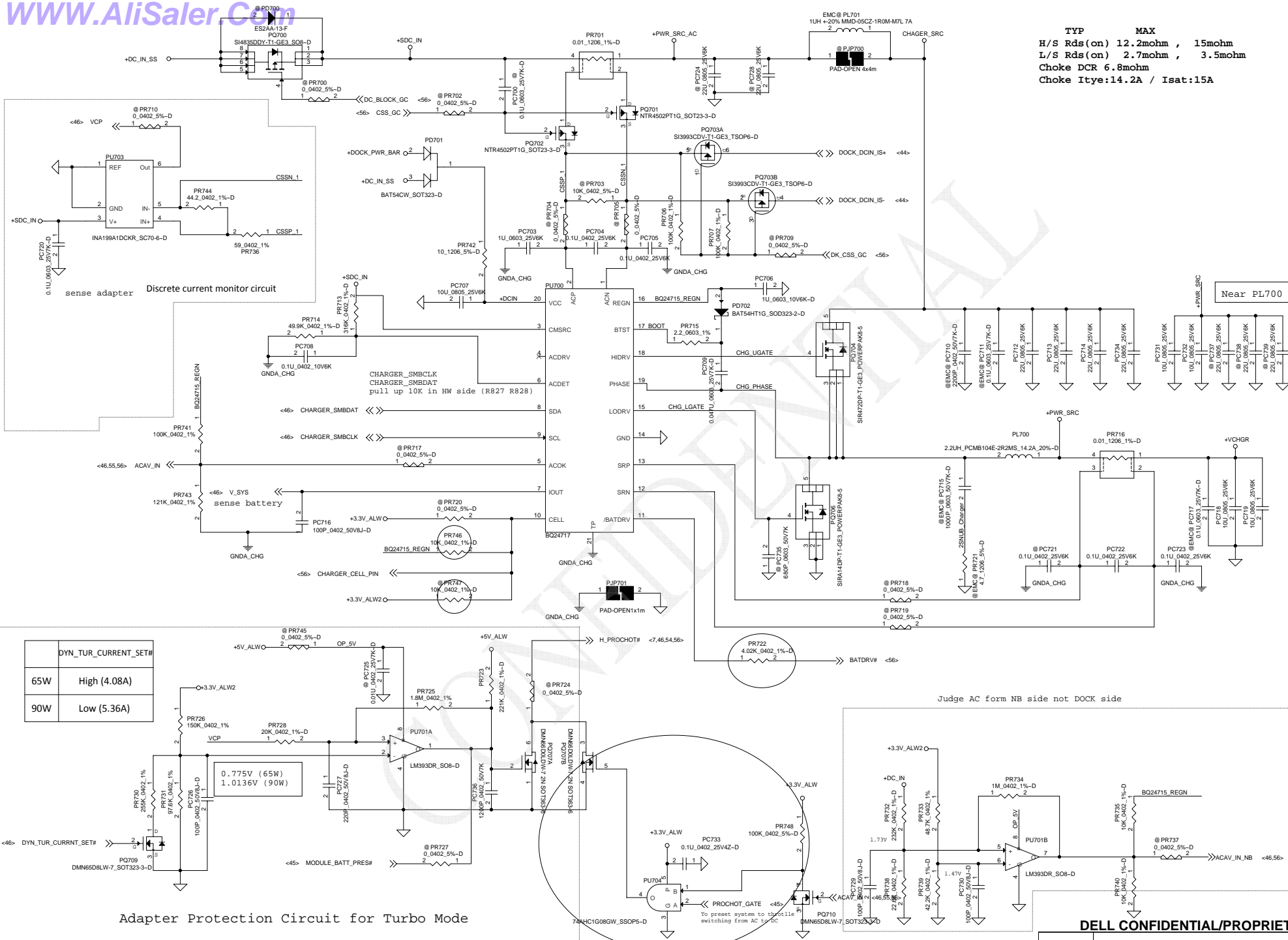
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TYP	MAX
H/S Rds(on)	12.2mohm , 15mohm
L/S Rds(on)	2.7mohm , 3.5mohm
Choke DCR	6.8mohm
Choke Itye:	14.2A / Isat:15A

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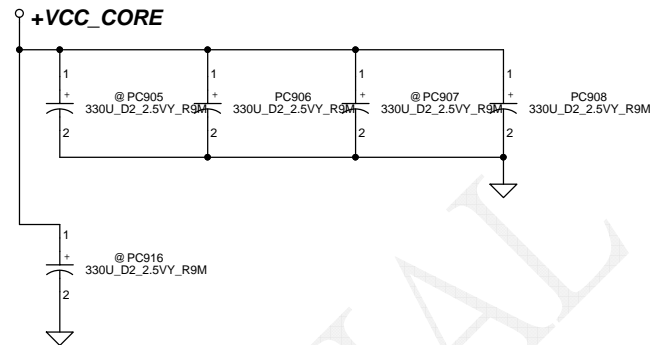
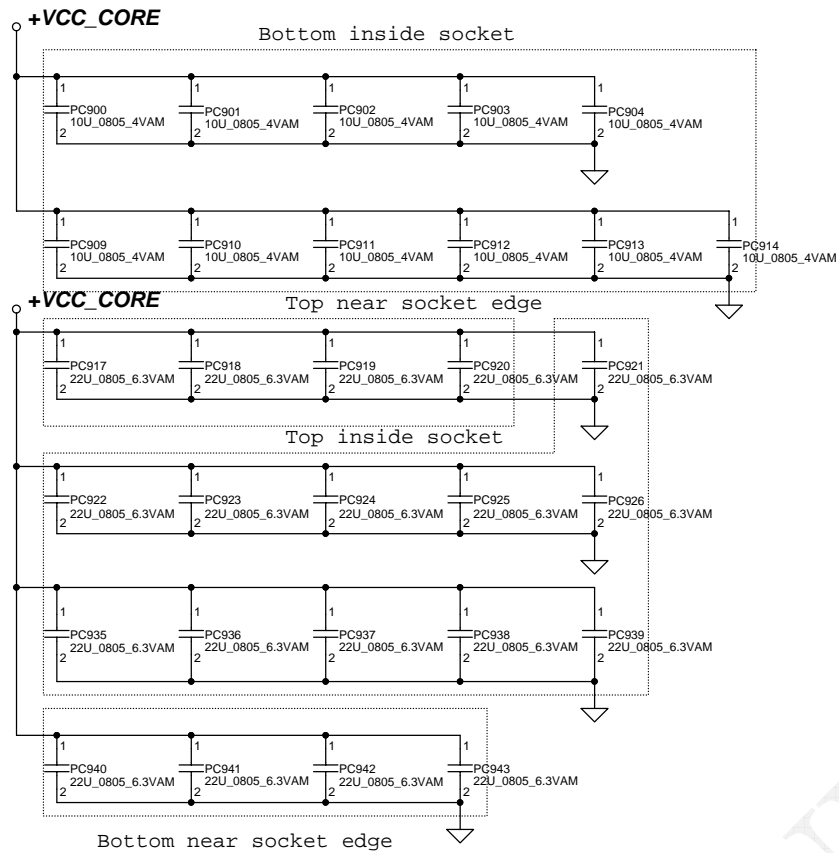
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Purpose: Trigger PROCHOT# when active battery is removed from system.
Allows EC to re-establish system performance for battery next in line.



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Version ChangeList (P. I. R List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	55	Charger	1/8	TI	Change cell pin pull high reference voltage from +3.3V_ALW to BQ24715_REGN	Depop PR720 and pop PR746	X01
2	55	Charger	1/8	Compal	Reduce adapter current ripple	PC724,PC728,PC712,PC713,PC714,PC734 pop 22uF cap	X01
3	54	VCC_CORE	1/8	Compal	Reduce adapter current ripple	Add input jump PJP500 and unpop PL504, PC516 pop 470pF	X01
4	55	Charger	1/8	Compal	Fine tune H_PROCHOT# response time.	Add 0402 cap PC736(1.2nF) on PU701 pin1	X01
5	55	Charger	1/8	Compal	Control H_PROCHOT# when AC removed.	Add PR727(0 ohm) and MODULE_BATT_PRES# connect to PU701 pin1	X01
6	54	VCC_CORE	1/8	Compal HW	Reduce noise on +VCCIO_OUT.	Add 1uF cap (PC548) on +VCCIO_OUT	X01
7	55	Charger	1/8	Compal	For Input current sense stabilize.	PC703 change from 0402 0.1uF to 0603 1uF	X01
8	56	Selector	1/8	Compal	For undock shutdown issue.	Add PQ827 and gate connect to SLICE_BAT_PRES#.	X01
9	55	Charger	1/8	Compal	To prevent charger into sleep mode when dual AC transient.	PC707 change from 1uF to 10uF.	X01
10	55	Charger	1/8	Compal	Improve ACAV_IN_NB ref voltage accuracy.	PR733 connect to +3.3V_ALW2. PR740 change from 12K to 10K PR733 change from 130k to 48.8k	X01
11	54	VCC_CORE	1/8	Intersil	Fine tune IMON, OCP, Iout offset.	Add PR558 4.99M on ISUMP to GND. PR535 change from 432 to 499. PR521 change from 95.3k to 97.6k. PC500 change from 0.01u to 1000p	X01
12	55	Charger	1/9	Compal	Fine tune ACOK response time.	PC708 change from 0.01uF to 0.1uF	X01
13	49 56	DCIN selector	1/9	Compal	GPIO net - AC_DIS# is high active. Corrent net name.	AC_DIS# cahnge to AC_DIS.	X01
14	55	Charger	1/9	Compal	Improve current sense accuracy.	PR744=44.2ohm PR736=59ohm	X01
16	56	Selector	1/9	Compal	AC_DIS circuit modify to improve output voltage level.	Add PD820,PQ828,PQ829,PR895.	X01
17	52	1.05V_VM	1/11	Compal	Improve output ripple voltage.	Add 10uF(PC310) on output.	X01
18	56	DCIN	1/14	Compal	To prevent PD2 damage issue, ESD team suggest pin5 connect to VCC.	PD2 pin5 connect to +3.3V_ALW, Add PR27 0 ohm PD2 change from NXP(SC300001100) to AMC(SC300001G00)	X01
19	55	Charger	1/17	Compal	Adapter protect from rating+0.75A to 2A.	PR730 change from 150k to 249k PR731 change from 66.5K to 162K unpop PR727	X01

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Version ChangeList (P. I. R List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	37,45,46	HW	12/10/2012	Compal	Change R-Pak package.	Change RP3,RP4,RP5,RP6,RP9,RP13 P/N to SD309100380 (Change Package from 1206 to 0804)	X01
2	46	HW	12/10/2012	Compal	Change R-Pak package.	Change RP8 P/N to SD309470180 (Change Package from 1206 to 0804)	X01
3	35	HW	12/10/2012	Compal	Change New TPM Chip	Change U39 P/N to SA00004WQ40 (AT97SC3204-DX4A12-ABF)	X01
4	28	HW	12/18/2012	Compal	Impact SMT DFX stilt and solder risk.	Change JLVDS1 P/N from SP01001BS00 to SP01001BT00	X01
5	20,35	HW	12/21/2012	DELL	Update GPIO map to rev 3.0C	Reserve PCH GPIO 22 for moving SP_TPM_LPC_EN from OUT65 and add PD 100k(RH332)	X01
6	30	HW	12/21/2012	Compal	Remove non-pop item	Remove R3730	X01
7	46	HW	12/21/2012	DELL	Update GPIO map to rev 3.0C	Add a 0ohm resistor R3772 to short RUNPWROK and AND_PWRGD	X01
8	28	HW	12/21/2012	DELL	ATG droop	Remove ATG config 1@,2@	X01
9	40	HW	12/26/2012	Compal	Support Samsung Galaxy Tab charger in S3.	Change USB charger P/N from SLGC55584AVTR to SLG55594AVTR	X01
10	38	ME	01/02/2013	Compal	JSIM1 BOSS hole update from 3.55mm to 1.7mm.	Change JSIM1 P/N from 159-1000302600 to 159-1000302602	X01
11	47	ME	01/03/2013	Compal	JSF1 pitch change from 0.5mm to 1.0mm	Change JSF1 P/N from A520620-SCHR22 to A090620-SAHR21	X01
12	30	HW	01/03/2012	COMPAL	Follow Realtek recommend circuit	1.Change R1119 ,R1120 to 100K 2.Change R1677 R1679 to 9.1 ohm 3.Change C958,C960 from 0603 to 0805	X01
13	28	HW	01/04/2013	COMPAL	Import U148 of 2nd source	U148 pin4 connect to +3.3V_ALW and remove C33	X01
14	15,17,18, 20,46	HW	01/04/2013	COMPAL	Import R-Pak	R820,R818,R829,R822 change to RP14 R1125,R810,R1156,R819 change to RP15 R814,R920,R870,R876 change to RP16 RH168,RH329,RH125 change to RPH18 R799,R835,R892,R869 change to RP17 RH138,RH136,RH257,RH201 change to RPH19 RH133,RH141,RH143,RH36 change to RPH20 RH122,RH166,RH171,RH217 change to RPH21 R1183,R513,R510,R775 change to RP18 R514,R767,R3,R805 change to RP19 R728,R739,R737,R716 change to RP20	X01
15	46	HW	01/07/2013	COMPAL	Change Board ID from X00 to X01	Change R875 from 240K to 130K	X01
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Version ChangeList (P. I. R List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
16	30	HW	01/08/2013	Compal	Follow EMI request	Change 0 ohm to 33 ohm bead for internal SPK (L702~L705)	X01
17	17,33,46	HW	01/08/2013	Compal	Follow Crystal EA result.	Change C470,471 from 33pF to 27pF Change CH13,CH14 from 18pF to 12pF Change CH743,CH741 from 22pF to 33pF	X01
18	38	HW	01/10/2013	Compal	W/O WWAN reapter EA test pass.	Remove WWAN repeater	X01
19	7,16	HW	01/11/2013	Compal	Follow ESD result.	Change RC35 from 0ohm to 0ohm short Pop RC52 1K Reserve C1553 0.047u	X01
20	28	HW	01/14/2013	Compal	To avoid +3.3V Camera inrush	De-populate C300 10U_0805 CAP	X01
21	27	HW	01/15/2013	Compal	The RTD2136R ripple is better than S version. The cap can be use small.	Change C109 from 22uF to 10uF.	X01
22	7	HW	03/11/2013	Compal	Follow Intel DG Rev.1.5	Change RC12 from 10k to 100k.	X01
23	6~45	HW	04/01/2013	Compal	For Vender recommend circuit	Change 0 ohm resistor to short pad Change Q63 P/N from SB00000RV00 to SB00000ZN00 Change L77 P/N from SM010022010 to SM01000MO00	X02
24	26	HW	04/01/2013	Compal	Follow Intel MOW Rev.1.0	Change R452,R450,R448,R449,R454,R453,R456,R455 from 680 ohm to 470 ohm.	X02
25	27	HW	04/01/2013	Compal	Follow Realtek recommend circuit	L60,L61,L62 change to R3881,R3882,R3883(Short 0ohm)	X02
26	39	HW	04/01/2013	Dell	Dell don't want EXP@ config on MB.	Remove EXP@ config	X02
27	24	HW	04/01/2013	Compal	Follow Icon Brightness result. Change LED resistance.	R955 from 1.8k to 680 ohm R938 from 1.8k to 680 ohm R958 from 620 to 680 ohm R934 from 1K to 1.8k ohm R3671 from 1.4k to 1.8k ohm R949 from 680 to 1.8k ohm	X02
28	36	HW	04/01/2013	Compal	OZ777FJ2 enters into test mode unexpectedly with SD card inserted incompletely.	Add R3884 1M ohm to GND on SD_CD#	X02

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Version ChangeList (P. I. R List)

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29	15~23,33	HW	04/01/2013	Compal	Intel ChipSet update.	Change PCH C1 version to C2 version. P/N from SA00006P30L to SA00006P33L	X02
30	13,14,40	HW	04/01/2013	Compal	Update OSCON P/N for DFX issue	Change 330uF OSCON P/N from SF000002Z00 to SF000003100 (CD14,CD36) Change 220uF OSCON P/N from SF000002Y00 to SF000003000 (C651)	X02
31	7	HW	04/01/2013	Compal	No support CPU XDP JTAG PU/PD issue	Pop RC59, RC62,RH52 for Intel recommend	X02
32	46	HW	04/01/2013	COMPAL	Change Board ID from X01 to X02	Change R875 from 130K to 33K	X02
33	37	HW	04/08/2013	COMPAL	Schematic synchronous	Change R725 PU Net from WIGIG60GHZ_DIS# to WIGIG60GHZ_DIS#_R Add R3885 to WLAN_RADIO_DIS#_R Add R3886 to BT_RADIO_DIS#_R	X02
34	7,16,32	HW	04/12/2013	COMPAL	The system running DVD will hand when doing ESD test.	Pop C1553, CC17,CC18,CH2 Add C1564 on PCH_PLTRST# (Non-pop) Add C1565 on PLTRST_EMB# (Non-pop) Add C1566 on H_THERMTRIP#_R (Non-pop) Add C1567 on XDP_RST#_R (Non-pop)	X02
35	24	HW	04/12/2013	COMPAL	Sleeve / Ring2 have EMI noise.	H20 connect from GND to NC for ESD request. (EMI AGND and DGND noise)	X02
36	35	HW	04/23/2013	COMPAL	TPM Chip Update.	Change TMP version from DX4A12 to X4A12. P/N from SA00004WQ40 to SA00004WQ50	X02
37	15~23,33	HW	06/04/2013	COMPAL	Change chip P/N to R3	Change PCH from R1 to R3. P/N from SA00006P33L to SA00006P35L Change PHY from R1 to R3. P/N from SA000050O3L to SA000050O5L	A00
38	46	HW	06/04/2013	COMPAL	Change Board ID from X02 to A00	Change R875 from 33K to 1K	A00
39	18	HW	06/04/2013	COMPAL	Change 0 0hm to short 0 ohm	Change RH177,RH178,RH179,RH181,RH182,RH183, RH184,RH185 to short 0 ohm	A00
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